

Shivaji University, Kolhapur



the Chancellor, Vice-Chancellor and
Members of the Management Council on
the recommendation of the Academic Council certify that

Patil Poonam Siddhu

Mother's Name - Mangal



has passed the

Doctor of Philosophy

in Electronics

in faculty of Science And Technology

on 11th September, 2020.

The said degree has been conferred on her

at Kolhapur in the Summer Session

of the year two thousand twenty one.

In Testimony *whereof are set the seal of the
university and the signatures of the Registrar and the Vice-Chancellor.*

Registrar



Vice-Chancellor

जी. आर. पळसे

एम.ए., एम.एड., सेट, नेट

प्र. संचालक, परीक्षा व मुल्यमापन मंडळ

G. R. Palse

M. A., M.Ed, SET, NET

I/c. Director,

Board of Examinations & Evaluation



शिवाजी विद्यापीठ,

कोल्हापूर-४९६ ००४, महाराष्ट्र

SHIVAJI UNIVERSITY,

KOLHAPUR-416 004, MAHARASHTRA

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Ref. No. :

Date :

Declaration of Result

It is hereby declared that the thesis entitled,

FPGA BASED FUZZY LOGIC SPINDLE TEMPERATURE COMPENSATION

Submitted by **Smt. Patil Poonam Siddu** in **Electronics** under the **Faculty of Science and Technology** is accepted by the University authorities for the award of the Degree of Doctor of Philosophy in **Electronics** under the Faculty of Science and Technology on **11-09-2020**.

I/c. Director

Board of Examinations & Evaluation

No. SU/PG/EXAM/Ph.D./2545

Date : 23 SEP 2020

To,

1. Smt. Patil Poonam Siddu
Department of Electronics,
Shivaji University, Kolhapur.
2. Dr.R.R.Mudholkar (Guide)
Department of Electronics,
Shivaji University, Kolhapur.
3. The Director,
Barr. Balasaheb Khardekar Knowledge Resource Center,
Shivaji University, Kolhapur.
4. The Head,
Department of Electronics,
Shivaji University, Kolhapur
5. Statement of Marks / Certificate Unit
6. Convocation Unit
7. Statistics Section
8. Copy to UGC Notification File



Estd: 1962
NAAC 'A' Grade

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No. SU / PG / EXAM / Ph.D. / 59 / 395

Date: 13 FEB 2021

Ph.D. Declaration Certificate

It is hereby certified that the thesis entitled,

FPGA BASED FUZZY LOGIC SPINDLE TEMPERATURE COMPENSATION

submitted by **Smt. Patil Poonam Siddu** in **Electronics** under the **Faculty of Science and Technology** is accepted by the University authorities for the award of the Degree of **Doctor of Philosophy in Electronics** under the **Faculty of Science and Technology** on **11-09-2020**.

It is hereby also certified that the Ph.D. awarded to **Smt. Patil Poonam Siddu** is in compliance of the provision of UGC (Minimum Standards and Procedure for the award of Ph.D. Degree Regulation 2009).

This certificate is issued in the light of UGC directive F.No.1/12002 (PS) Pt file III dated 28 August 2009 and as per decision of the Academic Council held on 11/02/2010.


Dy. Registrar