Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: **Dr. C. B. Patil** Programme: B.Sc. I Semester- II

Subject: Electronics Course Title: DSC-1005B Section- I Analog Electronics-II

Month: 1	March 2022	15th 2	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Bipolar Junction Transistor:	BJT: Structure, Working of transistor. Transistor configurations: CB, CE and CC,
10	0	10	A property As a property of the state of the	characteristics of transistor in CE and CB configurations, Current gains α and β , dc load line and Q point
	Selection of the control of the cont	GA COM	2) Unipolar Devices:	JFET: Construction, working and I-V characteristics MOSFET: Construction, working and I-V characteristics, UJT: introduction, structure and characteristics.
Month : A	April 2022		Module/Unit:	Sub-units planned
10	0	10	3) Amplifiers:	Transistor biasing - Fixed Bias and Voltage Divider Bias, Class A, B, AB and C Amplifiers, Single stage CE amplifier, Cascaded Amplifiers: Two stage RC, LC, TC and DC Coupled Amplifiers ,Concept of Differential amplifier and its advantages.
Month: N	lay 2022		Module/Unit:	Sub-units planned
10	Ö	10	4) Feedback Amplifier and Oscillators:	negative and positive feedback, Oscillators: Barkhausen criterion for sustained oscillations, Phase shift, Wein Bridge, Hartley and Colpitt's oscillator, UJT as relaxation oscillator

Dr. C. B. Patil

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Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: Dr. C. B. Patil Programme: B.Sc. II Semester- III

	ovember 20		itle: DSC-1005C Section-II Microprocess Module/Unit:	Sub-units planned
Lectures	Practicals 32	Total	Microcomputer Organization: Architecture of 8085 Microprocessor:	1) Components of microcomputer, RAM (SDRAM,DRAM), ROM Memory Interfacing and Memory Map
V-I time : politication (TU), exi-	midrow not on the control of the con	O has a consequence of the conse	Practicals Group A: 1. To design an Amplitude Modulator using Transistor/ Op-amp 2. To study envelope detector for demodulation of AM signal 3. To study FM - Generator / Detector 4. To study AM Transmitter / Receiver 5. To study FM Transmitter / Receiver	2) Features of 8085.Block diagram and Pin description of 8085. Data and address bus, Registers, ALU, Stack pointer, Program counter, Flag register, Clock and reset circuits. Interrupts in 8085.Demultiplexing of AD0-AD7.T-states, Machine cycle, Instruction cycle. Timing diagram of MOV and MVI instructions
Month: D	December 20	21	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	3) Instruction Set of 8085	3) classification of Instruction Set.
12 miles and a second s	32	44	Microprocessor: Practicals Group A: 6. To study Time Division Multiplexing (TDM) 7. To study Pulse Amplitude Modulation (PAM)8. To study Pulse Width Modulation (PWM) 9. To study Pulse Position Modulation (PPM) 10. To study ASK modulator	Addressing modes, Instruction set Data transfer, Arithmetic, logical, branch and control instructions
Month:	January 202	2	Module/Unit:	Sub-units planned
Lectures			4) Programming with 8085 Microprocessor:	4) Programs of Addition (8 and 10 bit), Subtraction, Multiplication,
	32			even numbers.

Dr. C. B. Patil



Dr. C. B. Patil Head

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: Dr. C. B. Patil Programme: B.Sc. II Semester- IV

Subject: Electronics Course Title: DSC-1005D Section-II Microcontroller 8051

Month: N	March 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	1) Introduction to 8051microcontroller: 2) Instruction Set of 8051:	1) features of 8051 family, Block diagram of 8051, Pin
12	32	44		description of 8051, , RAM structure of 8051, SFR's and GPR's in 8051,PSW register ,Clock and reset circuit, Memory organization ,I/O Ports.
rela limena se george di englished di dipatition di			Practicals Group B: 4. Division by repeated subtraction 5. Addition of 16-bit Numbers. 6. Use of CALL and RETURN Instruction. 7. Block data handling	2) Addressing modes. Instruction set of 8051: data transfer, arithmetic, Logical, Jump, call, Boolean instructions
Month: A	April 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	3) Timers, Serial port and	3) Timers in 8051, Timer
12	32	44	Interrupts(Assembly)programming of 8051: Practicals Group C: 1. Arithmetic operation using 8051 2. Logical operations using 8051 3. Study of timers of 8051 in mode 1 and mode 2 4. Study of interfacing of LED to 8051 microcontroller. 5. Study of interfacing of LED array	Registers, modes and Programming of timers, Serial ports: Serial port of 8051, modes, Registers Serial port, Serial port programming., Interrupt: Interrupt in 8051, Interrupt registers, Programming with interrupt
Month: N	May 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	4) Interfacing of Devices with 8051: Practicals Group C:	4) Introduction to embedded C, Data types in C, SFR accessing
12	32	44	6.Study of rotate instruction 7.Study of interfacing of 7-segment display 8. Study of interfacing Multiplexed 7-segment display. 9. Study of interfacing of stepper motor. 10. Study of interfacing of D.C motor.	, I/O programming, logical operations in C. Program to generate square wave on port pin, Interfacing of LED, Optocoupler, Switch, Relay, DC motor and Stepper motor

Dr. C. B. Patil

Dr. C. B. Patil Head

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: Dr. C. B. Patil Programme: B.Sc. III Semester- V

Subject: Electronics

Course Title: DSC-1005E1 Section - II 8051 Microcontroller Interfacing and Embedded C

	lovember 20		ction – II 8051 Microcontroller Interfact Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Introduction to embedded C Real World Interfacing of 8051	1) Data types, operators and loops, I/O programming,
12	40	52	Constitute Course At the Mannage of	Accessing SFR, Logical operation. Data conversion programs, Accessing ROM space, programming for Time delay generation (using timer), external interrupts
			Practicals: Group A: (LIC & PLC) 1. Instrumentation amplifier using OPAMP 2. Precision rectifier using OPAMP 3. Log amplifier using OPAMP Project work	2) Interfacing to output devices – LED, Relay, LCD, seven segment display, DC Motor, Stepper Motor.Interfacing to input device – Switch, 4X4 matrix keyboard, opto-coupler, thumb wheel switch. Interfacing of DAC0808 and ADC0804.
Month: December 2021		21	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	3) Serial communication in 8051	3) RS-232 standard and IC MAX-232, Concept of Baud rate
12	40	52	Practicals: Group A: (LIC & PLC) 4. Study of active filter: Low and High pass 5. Study of active filter: band pass 6. Study of V to F and F to V using PLL. Project work	SBUF register, SCON register, programming for data transmission and reception.
Month:	January 202	2	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	4) Applications of 8051	4) i) Gate Emulator (Logic Gate study using microcontroller) ii)
12	40	52	Practicals: Group A: (LIC & PLC) 7. Study of PLC Simulator and implementing Boolean function 8. Programming with PLC for sequential logic RS -FF,JK-FF,T-FF,D-FF 9. Study of PLC timers and counters in PLC Project work	Water level controller iii) speed control of DC motor iv) Temperature measurement using LM35, ADC0804, LCD. v)Bluetooth module interfacing. Vi) Speed control of Stepper Motor

Dr. C. B. Patil

Department of Electronics vivel(anand College, Kolhapur,

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: **Dr. C. B. Patil** Programme: B.Sc. III Semester-VI

Subject: Electronics
Course Title: Practicals

Month: N	1arch 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals: Group C: (Microcontroller 8051 & FPGA)	misses that th
0	40	40	1. Arithmetic and logical operations using 8051microcontroller. 2. Switch and Relay interfacing to 8051 microcontroller 3. DC motor interfacing to 8051microcontroller 4. Study of Timers in 8051 Microcontroller 5. Stepper Motor interfacing to 8051 microcontroller 6. DAC0808 interfacing to 8051microcontroller Project work	
Month: A	pril 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	
0	40	40	Group C: (Microcontroller 8051 & FPGA) 7. ADC0804 interfacing to 8051microcontroller. 8. Serial communication with PC using 8051microcontroller. 9. Write VHDL code to realize basic and derived logic gates. 10. Write VHDL code to realize Half adder, Full Adder using basic and derived gates. 11. Write VHDL code to realize Half subtractor and Full Subtractor using basic and derived gates Project work	A B AB IN SECTION OF THE SECTION OF
Month: N	May 2022		Lifetty sed Com-	Sub-units planned
Lectures	Practicals	Total	Practicals: Group C: (Microcontroller 8051 & FPGA)	
0	40	40	12. Design and simulation of a 4 bit Adder using VHDL. 13. Write VHDL code to realize Multiplexer (4x1) and Demultiplexer(1x4) using logic gates. 14. Write VHDL code to realize Decoder and Encoder using logic gates. 15. Write VHDL code to realize Clocked D, JK and T Flip flops (with Reset inputs) 16. Write VHDL code to realize 3-bit Ripple counter	A cost of the country
			Project work	

Dr. C. B. Patil



Dr. C. B. Patil

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. II Semester- III

Subject: Electronics Course Title: DSC-1005C Section-I Electronics Communication

Month: C	ctober 2021		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 1:Electronic Communication Practicals Group A:	Introduction to communication- meaning and types, Block
12	32	44	 To design an Amplitude Modulator using Transistor. To study envelope detector for demodulation of AM signal To study FM - Generator / Detector To study AM Transmitter / Receiver To study FM Transmitter /Receiver To study TDM. Pulse Amplitude Modulation To study Pulse Width Modulation 	diagram of an electronic communication system. Brief idea of frequency allocation for radio communication system in India (TRAI). Electromagnetic communication spectrum, band designations and usage. Channels and base band signals, concept of Noise, signal-to-noise (S/N) ratio.
Month: No	ovember 202	1	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 2: Analog Modulation-	Need for modulation, Amplitude
12	32	44	Demodulation Practicals Group A: 9. To study Pulse Position Modulation 10.To study ASK modulator 11.To study PSK modulator 12.To study FSK modulator	Modulation (AM) modulation index and frequency spectrum. Concept of DSB,SSB generation. Amplitude Demodulation (diode detector). PM: Concept only,
Month: D	ecember 202	21	Module/Unit:	Sub-units planned
Lectures 12	Practicals 32	Total 44	 Unit 2: Modulation- Demodulation Practicals Group B: 1. Addition and subtraction of numbers -direct addressing 2. Addition and subtraction of numbers -indirect addressing mode 3. Multiplication by repeated addition. 4. Division by repeated subtraction 	Frequency Modulation (FM), modulation index and frequency spectrum, equivalence between FM and AM. Generation of FM using VCO, FM detector (Slope detector), and Study of Super heterodyne radio receiver.
Month: Ja	anuary 2022		Module/Unit:	Sub-units planned
Lectures 12	Practicals 32	Total 44	Unit 3: Satellite communication Practicals Group B: 5. Addition of 16-bit Numbers. 6. Use of CALL and RETURN Instruction. 7. Block data handling	Introduction, Need, Geosynchronous satellite orbits, geostationary satellite, advantages of geostationary satellite. Satellite visibility, transponders (C- Band), path loss, ground station, simplified block diagram of earth station. Uplink and down link.

Mr. P. R. Bagade

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Dr. C. B. Patil

Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. II Semester- IV

ubject: El			Module/Unit:	Sub-units planned
	Practicals		Unit 1:Analog Pulse Modulation Practicals Group C:	Channel capacity, Sampling theorem, Basic Principles-PAM, PWM, PPM, modulation and
12	32	44	 Arithmetic operation using 8051 Logical operations using 8051 Study of timers of 8051(mode 1& 2) Interfacing of LED to 8051. Study of interfacing of LED array Study of rotate instruction Study of interfacing of 7-segment Study of interfacing multiplexed 7-segment display 	detection technique for PAM only.
Months A	pril 2022		Module/Unit:	Sub-units planned
Lectures	pril 2022 Practicals	Total	Unit 2: Digital Pulse Modulation	Need for digital transmission,
12	32 May 2022	44	Practicals Group C: 9. Interfacing of stepper motor. 10. Study of interfacing of D.C motor. Practicals Group D: 1. Designing of PCB using Software 2. Development of PCB 3. Soldering techniques 4. Temperature Controller using IC 741 /8051. Module/Unit:	Pulse Code Modulation, Digital Carrier Modulation Techniques, Sampling, Quantization and Encoding. Concept of Amplitude Shift Keying (ASK), Frequency Shift Keying (FSK), Phase Shift Keying (PSK), and (BPSK and QPSK). Sub-units planned Basic concept of mobile
Lectures		Total	Unit 3: Mobile Telephony System	communication, frequency
12	32	44	 Practicals Group D: 5. Designing of Variable Power Supply using LM 317. 6. Build Regulated Power Supply using IC 7805 7. Build Dual Power Supply using IC 7809 and IC 7909 	bands used in mobile communication, concept of cel sectoring and cell splitting Hand-off process, SIM number IMEI number.
Month	: June 2022		Module/Unit:	Sub-units planned
Lecture		s Total	Practicals Group D. 8. Assemble Electric Board with	Architecture (Block diagram) of mobile communication network,
12	32	44	switches, sockets and MCB and measure Voltage, Current and Power for given device 9. Study and fault finding of: Fan /Iron/ Mixer/Cell phone Charger 10.Build Lighting system using LEI	idea of GSM, CDMA, TDMA and FDMA technologies, simplified block diagram of mobile phone handset, 2G, 3G and 4G concept (qualitative only). GPS navigation system.

JP Bar Mr. P.R. Bagade

bseH Department of Electronics Vivekanand College, Kolhapu-

Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. III Semester-V

Subject: Electronics Course Title: DSC-1005E2 Section - II Antenna and Wave Propagation

Month : C	october 2021		Module/Unit:	Sub-units planned
Lectures 12	Practicals 40	Total 52	Unit 1: Antenna Basics GROUP A: (LIC & PLC) 1. Instrumentation amplifier 2. Precision rectifier using OPAMP 3. Log amplifier using OPAMP 4. Active filter: Low and High pass 5. Study of active filter: band pass 6. Study of V to F and F to V.	Antenna: Function of antenna. Radiation Mechanism. Antenna Parameters: Radiation pattern, Main Lobe and Side Lobes, Half-power beamwidth, Radiation intensity, Antenna efficiency, Directivity, Gain, BW, Polarization, input impedance, radiation efficiency.
SCHOOL SECTION	ovember 202		Module/Unit:	Sub-units planned
Lectures 12	Practicals 40	Total 52	Unit 2: Antenna as a Transmitter GROUP A: (LIC & PLC 7. Study of PLC Simulator and implementing Boolean function 8. Programming with PLC for sequential logic RS, JK-FF, T, D-FF 9. Study of timers and counters in PLC	Power delivered to antenna, Input impedance. Radiation from an infinitesimal small current element, Radiation from an Hertzian dipole, Reactive, Induction and Radiation fields, radiation resistance for small current element.
Month: I	December 202	21	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 3: Radiating wire Structures	Monopole, Dipole, Folded dipole, Loop antenna and
12	40	52	 GROUP B: (Antenna and Power Ele) Study of simple dipole λ/2 antenna Study of folded dipole λ/2 antenna Study of simple dipole λ/4 antenna Study of Yagi- Uda with 3 and 5 ele. Study of SCR characteristics Study of AC / DC Timer SCR firing by UJT AC Voltage controller Speed Control of DC Motor 	dipole, Loop antenna and Biconical broadband Antenna. Basics of Patch Antenna and its design. Examples of Patch antenna like bowtie, sectoral, fractal. Concept of smart antenna.
Month: J	January 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 4: Radio Wave Propagation	Structure of atmosphere, Ground
12	40	52	GROUP B: (Antenna and Power Electronics) 10.ON/OFF Temperature controller 11.Phase Shift control of SCR 12.Simulation of single phase half wave controlled rectifier with R & RL 13.Simulation of single phase HW controlled bridge rectifier with R.	wave, Space Wave, Sky Wave Propagation - Introduction Structure of Ionosphere Refraction and Reflection of Sky Waves by Ionosphere, Ray Path Critical Frequency, MUF, Virtua Height and Skip Distance Relation between MUF and skip Distance, Multi-hop Propagation

Mr. P. R. Bagade



Dr. C. B. Patil Head

Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. III Semester- VI

Subject: El	arch 2022		Course Title: DSC-1005F2 Section – I I Module/Unit:	Sub-units planned
	*7	Total	Unit 1:Power semiconductor	Definition, Need- semiconductor power devices, classification of power semiconductor devices,
12	40	52	 Arithmetic and logical operations using 8051 μC. Switch and Relay interfacing to 8051 μC. DC motor interfacing to 8051 μC. Study of Timers in 8051 μC. Stepper Motor interfacing to 8051 μC. DAC0808 interfacing to 8051 microcontroller. ADC0804 interfacing to 8051 microcontroller. Serial communication with PC Write VHDL code to realize basic and derived logic gates. Write VHDL code to realize Half & Full Adder. Write VHDL code to realize Half 	Power diode: structure, operation, conductivity modulation, I-V characteristics, Reverse recovery effect, series and parallel connection of diode, Power transistor: structure, operation, effect of drift layer. Switching characteristics, specifications, Power MOSFET: MOSFET structure, characteristics, operation and drive circuits
al galle	Maria Paris		subtractor and Full Subtractor.	2 1 1 2 4
Month: A	April 2022	distrib	Module/Unit:	Sub-units planned
Lectures		Total	Unit 3: Thyristors	Types of Thyristors, Structure of
12	40	52	12.Design and simulation of a 4 bit	SCR, SCR Characteristics, two
Name of		SIDIL	Adder using VHDL.	transistor analogy - Methods of
	AND THE PARTY OF	A. har	13. Write VHDL code to realize	turning ON and turning OFF
	REPROFIT		MUX (4x1) and De-MUX (1x4)	
	4	1354	using logic gates.	protection circuits
			14. Write VHDL code to realize	
			Decoder -Encoder	working and V-I characteristic.
			15. Write VHDL code to realize	Little State of the Little
		THE S	Clocked D, JK and T Flip flops	Sub-units planned
Month :	May2022		Module/Unit:	Basics of single and three phas
Lecture	s Practicals	Total		. I line voltee
12	40	52	GROOT D.	waveforms, SCR as a stat
	77 = 10	manufi.	Instrumentation)	Transferring,
NA CONTRACTOR			Interfacing of Switches and LED in Addition	rectification, single phase ha
mit-ann	district to a		with Arduino	wave, full wave, bridge rectifie
	Company of		2. LCD Interfacing with Arduino	with resistive & inductive loads
	P 258		Stepper Motor Interfacing Interface temperature senso	
		HEIGH	LM35 with Arduino board.	
				or COLL
1	783		5. Interface temperature sensor Humidity Sensor (DHT11) wit	100
No.	3) 4 30			Z ES WE B
	L 15		Arduino ///	100 100

Month: J	une2022	1 3 3 1	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 4:	Power Supplies: Switch mode
12	40	52	Power Systems GROUP D: (AVR and Instrumentation) 6. Accelerometer Sensor Interfacing with Arduino. 7. Study of temperature sensor RTD and Thermister. 8. Function generator using IC 8038 9. Automatic Porch light control using LDR and relay. 10.Study of the characteristics of RTD 11.To study transducer (Thermistor/ Thermocouple)	power supply (DC): flyback, forward, half bridge and full bridge converters. Uninterrupted power supply (UPS), Electronic Ballast, Power factor correction.

Mr. P.R. Bagade



Dr. C. B. Patil

Department of Electronics
Academic Year: 2021-22
Annual Teaching Plan

Name of the Teacher: Mr. N. P. Mote Programme: B.Sc. I Semester- I

Subject: Electronics

Course Title: Practicals

Month : C	October 2021		Module/Unit:	Sub-units planned
Lectures 0	Practicals 16	Total 16	Practicals: 2. Measurement of Amplitude, Frequency & Phase difference using Oscilloscope. 3. Verification of (a) Thevenin's theorem and (b) Norton's theorem. 4. Verification of Superposition Theorem.	
Month: N	ovember 202	1	Module/Unit:	Sub-units planned
Lectures 0	Practicals 16	Total 16	Practicals: 5. Verification of the Maximum Power Transfer Theorem.6. Study of the I-V Characteristics of (a) p-n junction Diode, and (b) Zener diode. 7. Study of (a) Half wave rectifier and (b) Full wave rectifier (FWR).	interval and
Month: I	December 202	21	Module/Unit:	Sub-units planned
Lectures 0	Practicals 16	Total 16	Practicals: 8. Study the effect of (a) C- filter and (b) Zener regulator on the output of FWR. 9. Study of the I-V Characteristics of UJT and design relaxation oscillator. 10. Study of the output and transfer I-V characteristics of common source JFET.	pesmon intendiction of the second sec
Month: J	January 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals: 11. Study of Fixed Bias and Voltage divider bias	American Indiana Indiana
0	16	16	configuration for CE transistor. 12. Design of a Single Stage CE amplifier of given gain. 13. Study of the RC Phase Shift Oscillator.	emiceum Squem (3A enel 7 pp. (3) standard reggmin I dhold

Mr N. P. Mote

COLLEGE SESSION

Dr. C. B. Patil

Head

Department of Electronics
Vivekanand College, Kolhapuli

Department of Electronics

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Department of Electronics Academic Year: 2020-21 Annual Teaching Plan

Name of the Teacher: Mr. N. P. Mote

Programme: B.Sc. II Semester- III and IV

Subject: Electronics

Course Title: Practicals

Month: C	Month: October 2021		Module/Unit:	Sub-units planned
Lectures 0	Practicals 16	Total	Practicals: Group B: (Antenna and Power Electronics)	
	andne l		 Study of simple dipole λ/2 antenna Study of folded dipole λ/2 antenna Study of simple dipole λ/4 antenna Study of Yagi-Uda with 3 and 5 element simple dipole antenna Study of SCR characteristics (static) Project work 	Monde Mesografia
Month: N	ovember 202	1	Module/Unit:	Sub-units planned
Lectures 0	Practicals 16	Total 16	Practicals: Group B: (Antenna and Power Electronics) 6. Study of AC / DC Timer 7. SCR firing by UJT	01
	340-01		8. AC Voltage controller 9. Speed Control of DC Motor. 10. Study of ON/OFF Temperature controller (LM34/LM35/AD590)	-Month December
			11. Phase Shift control of SCR Project work	
Month: I	December 202	21	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals: Group B: (Antenna and Power Electronics)	
0	16	16	12. Study of Introduction to MATLAB/Scilab 13. To study the simulation of single phase half wave controlled rectifier with R & RL-load using MATLAB - simulink/Scilab 14. To study the simulation of single phase half controlled bridge rectifier with R using MATLAB - simulink/Scilab	present simple
Month: J	January 2021		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals: 15. Amplitude Modulation-Modulation &	
0	16	16	Demodulation using MATLAB & Simulink/Scilab 16. Sampling Theorem using MATLAB & Simulink/Scilab Project work	

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Department of Electronics (iveleased College, Kolhapel

Mr N. P. Mote

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Dr. C. B. Patil

Head

Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the Teacher: Mr. N. P. Mote

Programme: B.Sc. III Semester- V and VI

Subject: Electronics Course Title: Instrumentation, Antenna and Wave Propagation

Subject: E Month: Oc	tober 2021		Module/Unit:	Sub-units planned
Lectures			Measurements, Instrument & Calibration:-	Basics of Measurements: Accuracy, Precision, resolution, reliability,
12	40	52	Practicals: Group D: 1. Automatic Porch light control using LDR and relay. 2. Study of the characteristics of Resistance Temperature Detector (RTD) Project work	repeatability, validity, Errors and their analysis, Standards of measurement. Instrument: Static and Dynamic characteristics of instruments, dead zone, hysteresis, threshold, resolution, input & output impedance, loading effects. Calibration of instruments and Standards
Month: N	ovember 202	21	Module/Unit:	Sub-units planned
Lectures	Practicals 40	Total 52	2) Transducers and Sensors:- Practicals:	Definition, Classification of Transducers, Selection criterion for Transducers, Detail Study of
12 40	. 32	Group D: 3. Study of LVDT 4. Study of Temperature Sensor LM	Transducers: Thermister, RTD, Thermocouple, Strain gauge, LVDT, Capacitive transducer	
		35 5. Study of Instrumentation Amplifier Project work	(microphone), Opto-electric transducer – LDR, Photo diode, PIR , Loud speaker, Piezoelectric transducer, Proximity sensor Inductive, capacitive.	
Month: De	ecember 202	1	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	3) Signal Conditioning and Data Acquisition System:	Introduction, Sample and Hold circuit, Thermister Wheatstone
12	40	52	Practicals: 6. Study of Timers 7. Study of interfacing of LED and Relay to 8051	bridge amplifier, Instrumentation amplifier, Attenuator, Introduction to Data Acquisition System (DAS), Single channel & multi channel DAS. Data logger.
Month: January 2021			Module/Unit:	Sub-units planned
Lectures	Practicals	Total	4) Digital Instruments:-	Introduction to digital instrument:
12	40	52	Practicals: Group D: 6. Interfacing of Switches and LED with Arduino/AVR microcontroller. 7. LCD Interfacing with Arduino/AVR microcontroller.	Advantages of Digital instruments, Digital Tachometer, Digital Capacitance meter, Digital Phase Meter, Digital Frequency Meter. Digital Multi-meter,

Mr N. P. Mote

Dr. C. B. Patil Head

Department of Electronics Academic Year: 2020-21 Annual Teaching Plan

Name of the Teacher: Mr. N. P. Mote

Programme: B.Sc. II Semester- III and IV

Subject: Electronics

Course Title: Practicals

Month: O	ctober 2021		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals: Group B : (Antenna and Power Electronics)	
0	16	16	 Study of simple dipole λ/2 antenna Study of folded dipole λ/2 antenna Study of simple dipole λ/4 antenna Study of Yagi-Uda with 3 and 5 element simple dipole antenna Study of SCR characteristics (static) Project work 	
Month: N	ovember 202	1	Module/Unit:	Sub-units planned
Lectures 0	Practicals 16	Total 16	Practicals: Group B: (Antenna and Power Electronics) 6. Study of AC / DC Timer 7. SCR firing by UJT 8. AC Voltage controller 9. Speed Control of DC Motor. 10. Study of ON/OFF Temperature controller (LM34/LM35/AD590) 11. Phase Shift control of SCR Project work	of participation of the partic
Month : I	December 202	2.1	Module/Unit:	Sub-units planned
Lectures 0	Practicals 16	Total 16	Practicals: Group B: (Antenna and Power Electronics) 12. Study of Introduction to MATLAB/Scilab	
	u-dr2	sou an	13. To study the simulation of single phase half wave controlled rectifier with R & RL-load using MATLAB - simulink/Scilab 14. To study the simulation of single phase half controlled bridge rectifier with R using MATLAB - simulink/Scilab	Technol thousand
Month: January 2021			Module/Unit:	Sub-units planned
Lectures	A DESCRIPTION OF THE PROPERTY.	Total	Practicals: 15. Amplitude Modulation-Modulation &	
0	16	16	Demodulation using MATLAB & Simulink/Scilab 16. Sampling Theorem using MATLAB & Simulink/Scilab Project work	annia de la compania

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Department of Electronics
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Mr N. P. Mote

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Dr. C. B. Patil

Head

Department of Electronics Academic Year: 2021-22 **Annual Teaching Plan**

Name of the teacher: Dr. P. S. Jadhav Programme: B.Sc. I Semester-I

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Subject: Electronics Course Title: DSC-1005A Section-II: Digital Electronics-I

	Month :	October 202	21	Module/Unit:	Sub-units planned
	Lectures	Practical	Total	Lecture: Unit 1:Number System, Binary Codes and Binary	Decimal, Binary, Octal and Hexadecimal number systems
	12	32	44	Arithmetic Practical's: Group A 1. To familiarize with basic electronic components (R, C, L, diodes, transistors), Digital Multimeter, Function Generator, power supplies and Oscilloscope etc. 2. Measurement of Amplitude, Frequency & Phase difference using Oscilloscope. 3. Verification of Thevenin's Theorem.	and their inter conversions.BCD code. ASCII code, Gray Code, Excess-3 Code, Binary Arithmetic: Addition, Subtraction by 1's complement and 2's complement method, Representation of signed and unsigned numbers
				Unit 2:Logic Gates, Boolean algebra:	Study of logic Gates: OR, AND, NOT, NOR, NAND, XOR, XNOR, Universal Gates, Boolean identities and Law's.
	Month: No	ovember 20	21	Module/Unit:	Sub-units planned
	Lectures 12	Practical 32	Total	Lectures : Unit 2:Logic Gates, Boolean algebra:	Fundamental theorems of Boolean algebra. Standard
d Electronics ge, Kolhap	Head funent of sand Coffe	Desa Vivekar		Practicals: 1. Verification of Superposition Theorem. 2. Study of the I-V Characteristics of P-N junction Diodes. 3. Study of the breakdown Characteristics of Zener Diode	representation of logic functions (SOP and POS), Minimization Techniques (Karnaugh map minimization up to 4variables for SOP). Arithmetic Circuits: Binary Addition. Half and Full Adder. Half and Full Subtractor, 4-bit binary Adder/Subtractor.
				Unit 3:Logic Families	Logic Families: Types of Logic Families, Characteristics of Logic Families, TTL NAND gate, TTL NOR gate, TTL NOT gate

Month I	Danamhar 20	221	Madula II Iaita	Sub units aloused
Month: 1	December 20)21	Module/Unit:	Sub-units planned
Lectures	Practical	Total	Lecture: Unit 3:Logic Families	Concept of Tristate Logic, MOS Technology, CMOS
12	32	44	Practicals: 1. Study of Full wave rectifier 2. Study of Logic Gates. 3. Study of Universal Gates using fundamental gates.	NOR,NAND and NOT gates. Comparison of TTL and CMOS logic families.
Mont	th: January	2022	Module/Unit:	Sub-units planned
Lectures	Practical	Total	Unit 4:Combinational circuits:	Multiplexers: - 2 to 1, 4 to 1
12	32	44	 Verification of Norton's Theorem. Study of De-Morgans Theorems. Study of Half wave rectifier 	and 8 to 1.Demultiplexer: - 1 to 2,1 to 4, 1 to 8. Encoder: concept of encoder, Decimal to BCD Encoder. Basic Binary decoders: 2 to 4 line, 3 to 8 line and 4 to 16 line, BCD to decimal decoder, Study of BCD to seven-segment decoder driver IC 7447.

Dr. P. S. Jadhav

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Dr. C. B. Patil



Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the teacher: Dr. P. S. Jadhav Programme: B.Sc. I Semester- II

	Subject: 1	Electronics C	Course Ti	itle: DSC-1005B Section- II Digital Electronics-II			
	Mor	nth: March 2	022	Module/Unit:	Sub-units planned		
	Lectures	Practicals	Total	Unit 1: Sequential Circuit Practical: Group B	Concept of Flip-flop, RS, D and JK Flip-Flops Concept		
d Electronics ge, Kolhapur	angu una		44	 Study of Half Adder and Full Adder Study of Multiplexer (4:1) and Demultiplexer (1:4) Study of I-V Characteristics of JFET. Study of Input and Output Characteristics of CE configuration of BJT 	of Clock, Level and Edge Triggered RS, D, JK FF. Preset and Clear operations. Race-around conditions in JK Flip-Flop, Master-slave JK Flip-Flop, T-Flip-flop.		
		th: April 20		Module/Unit:	Sub-units planned		
	Lectures	Practical	Total	Unit 2: Shift registers and counters	Concept of register, Left		
	12	32	44	Practical: 1. Wein Bridge Oscillator. 2. Study of the Colpitt's oscillator 3. Study of the Hartley oscillator. 4. Building and testing of RS Flip-Flop using NAND/NOR gate	shift and Right Shift operations, Types of shift registers: SISO, SIPO, PISO & PIPO (only up to 4 bits). Counters: classification of counters, Asynchronous counters: 3 bit ripple counter, Decade Counter. Synchronous Counter: 3 bit and decade synchronous counter. Ring Counter and Johnson Counter. Applications of Counters.		
		nth: May 202		Module/Unit:	Sub-units planned		
	Lectures	Practical	Total	Unit 3: Data Converters Practical: 1. Building and testing D and JK	4 bit binary weighted and R- 2R ladder network DAC:		
	12	32	44	Flip-Flop using IC Design and study of an Astable Multivibrator using IC 555 Timer. Design and study of a Monostable Multivibrator using IC 555 Timer. computer simulations: Design clocked SR and JK Flip-Flops using Gates. Design 4-bit asynchronous counter using Flip-Flop ICs	circuit and working. DAC Characteristics: Accuracy and Resolution. ADC: Flash, Counter type, successive approximation ADC, ADC Characteristics.		
				ESTD M			

Month: June 2022		22	Module/Unit:	Sub-units planned	
Lectures	Practical	Total	Unit 4: Study of Timer IC555	IC555 timer: Introduction, Block diagram, Astable,	
12	32	44	Total Sept Sept Sept Sept Sept Sept Sept Sept	Monostable and Bistable multivibrator circuits. Applications of IC555: PWM, square wave generator and FSK.	
ine-tir?		i	Walinday of California (California)		

Dr. P. S. Jadhav

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Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: **Dr. P. S. Jadhav** Programme: B.Sc. III Semester-V

Subject: Electronics Course Title: DSC-1005E2Section - I Linear Integrated Circuits

	Month	October 20	21	Module/Unit:	Sub-units planned	
1	Lecture	s Practical	s Total	Unit 1 Operational	ac amplificity Differential amplification	
nid d Electronics ge, Kolhapur	and Colle	Vivekan	12	Amplifier and Linear IC's	Emitter coupled differential amplifier with Operation, characteristics and parameters (impedances, common mode and differential mode gain, CMRR), Dual input and single en output configuration of differential amplification Method to improve CMRR (constant current band current mirror bias), Introduction to open block diagram of op-amp, offset balance technique of op-amp, drift Parameters of op-ar study of IC 741 and comparative study of IC OP 07, LM324, AD620	
	Month	: Novemebe	er 2021	Module/Unit:	Sub-units planned	
	Lectures	Practicals	Total	Unit 2: Applications	Virtual ground concept, Op-amp as inverting and non-inverting amplifier, summing amplifier	
	12	0	12	of Op-amp	(adder and Subtractor), V to I and I to V converter voltage follower, bridge amplifier, Differentiator and integrator, log and antilog amplifier. Op-amp as comparator, regenerative comparator (Schmitt trigger), sine wave oscillators (phase shift and Wien -bridge), Triangular wave generator, square and pulse generator. Peak detector, clipping and clamping circuits.	
		ecember 202		Module/Unit:	Sub-units planned	
	Lectures	Practicals	Total	Unit 3:	Precision rectifier, Op-amp as precision rectifier,	
	12	0	12		Absolute value precision rectifier. Advantage of active filters over passive filters. Different types of active filters. Study and design of low pass, high pass, band pass and band stop filters Study of filter response (Butterworth, Chebyshev.)	
	Month: Ja	nuary 2022			Sub-units planned	
	Lectures	Practicals	Total	Unit 4: Phase	Block diagram of PLL with functioning of each	



12	0	12	Locked Loops (PLL)	block, calculation of capture range and lock range frequencies, application of PLL (frequency multiplier, FM modulator, frequency synthesizer and FSK) Study of IC565, IC8038. IC555 timer as variable duty cycle (10% to 90%), sequential timer, ramp generator.
	*		and the property of	validati A. A. A. O mobile with some No. P. S. dedian V memorali Section B. So. III Sometra

Dr. P. S. Jadhav

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Dr. C.B. Patil



Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the teacher: Dr. P. S. Jadhav

Programme: B.Sc. III Semester- VI

Subject: Electronics Course Title: DSE-1005F2 Section- II FPGA& VHDL Programming

M	onth: March 20)22	Module/Unit: Sub-units planned			
171	onthi. Water 20	122	Module/Unit:	Sub-units planned		
Lectures	Practicals	Total	Unit I: Introduction to Programmable Logic	Evolution of Programmable logic devices, PAL, PLA and GAL. CPLD and FPGA		
12	0	12	Devices	architectures. Placement and routing. Logic Cell structure, Programmable interconnects, Logic blocks and I/O Ports. Clock distribution in FPGA		
(TRA or o		m-duza Matan Matan Matan	Unit: II Basics of VHDL	Introduction: Introduction to Computer- aided design tools for digital systems. Hardware description languages, introduction to VHDL, data objects		
Mo	onth : April 202	22	Module/Unit:	Sub-units planned		
Lectures	Practicals	Total	Unit 2: Basics of VHDL	classes and data types, operators,		
12	0	12	Line as alternations, seeks	overloading, logical operators, Types of delays, Entity and Architecture declaration, Introduction to behavioral, dataflow and structural models.		
M	onth: May 202	2	Module/Unit:	Sub-units planned		
Lectures	Practicals	Total	Unit 3: VHDL	VHDL statements: Assignment statements,		
H Inemhea 3 bneas		12	Programming	sequential statements and process, conditional statements, case statement, Array and loops, resolution functions, packages and Libraries, concurrent statements. Subprograms: Application of Functions and Procedures, Structural Modelling, Component declaration, structural layout and generics		
Mo	Month: June 2022		Unit 4: Sequential and Combinational Circuit	VHDL Models and Simulation of		
Lectures	Practicals	Total	Design:	combinational circuits such as Multiplexers, Demultiplexers, encoders ,decoders, code converters, comparators, implementation		
12	0	12	Minist Dept. Families	of Boolean functions etc. Sequential Circuits Design: VHDL Models and Simulation of sequential Circuits, Shift Registers, counters etc.		

Dr. P. S. Jadhav

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Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: **Dr. P. S. Jadhav** Programme: B.Sc. III Semester-VI

Subject: Electronics Course Title: SEC 4: Introductions to Arduino and IoT

Month: M	larch 2022		Module/Unit:	Sub-units planned	
Lectures	Practicals 0	Total 6	1) Introduction to Arduino Board & Accessories :	The Arduino Platform, Block diagram, Architecture, Pin functions, overview of main features such as I/O Ports, Timers, interrupts serial port, PWM, ADC etc.	
Month: April 2022		Module/Unit:	Sub-units planned		
6	0	6	2) Display Interfacing:	Interfacing Arduino to LED's- blinking single LED, blinking multiple LED's, 7 segment display	
Month: M	ay 2022		Hilly le rocket S and	Lant District Description	
6	0	6	2) Display Interfacing:	Traffic light ,LED flashes ,LED dot matrix . Interfacing to LCD's- Basic LCD control, display a message on LCD screen.	

Dr. P. S. Jadhav

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Dr. C. B. Patil

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: **Dr. Milind S. Patil** Programme: B.Sc. III Semester- V

Subject: Electronics
Course Title: Practicals

Month: (October 2021		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	
	40	40	Group B: (Antenna and Power Electronics) 1. Study of simple dipole λ/2 antenna 2. Study of folded dipole λ/2 antenna 3. Study of simple dipole λ/4 antenna 4. Study of Yagi-Uda with 3 and 5 element simple dipole antenna 5. Study of SCR characteristics (static) Project work	
Month: N	ovember 202	21	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	
0	40	40	Group B: (Antenna and Power Electronics) 6. Study of AC / DC Timer 7. SCR firing by UJT 8. AC Voltage controller 9. Speed Control of DC Motor. 10. Study of ON/OFF Temperature controller (LM34/LM35/AD590) 11. Phase Shift control of SCR Project work	Contractor months
Month : I	December 202	21	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	p.mvu
0	40	40	Group B: (Antenna and Power Electronics) 12. Study of Introduction to MATLAB/Scilab 13. To study the simulation of single phase half wave controlled rectifier with R & RL-load using MATLAB - simulink/Scilab 14. To study the simulation of single phase half controlled bridge rectifier with R using MATLAB - simulink/Scilab	
Month: Ja	anuary 2022	atrolii [Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals: 15. Amplitude Modulation-Modulation &	
0	40	40	Demodulation using MATLAB & Simulink/Scilab 16. Sampling Theorem using MATLAB & Simulink/Scilab Project work	

Dr. Milind S. Patil

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Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: **Dr. Milind S. Patil** Programme: B.Sc. III Semester- VI

Subject: Electronics Course Title: DSE1005 F1 Section II Advanced Microcontroller

Month: N	farch 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	1) Embedded Systems Design:	1) What is embedded
12	40	52	2) Introduction to AVR microcontroller:	system, embedded system basic blocks, embedded
			Practicals:	system hardware and
			Group D: (AVR and	software, embedded systen
	l'ambent		Instrumentation)	characteristics, embedded
			1. Interfacing of Switches and LED	system applications.
		1	with Arduino/AVR microcontroller.	2) Overview of AVR
			2. LCD Interfacing with	family, ATmega8 pin
			Arduino/AVR microcontroller.	configuration & function of
S RIVERS THE			3. Stepper Motor Interfacing with	each pin. AVR
X 25 1		The same of	Arduino/AVR microcontroller.	Microcontroller
		-	4. Interface temperature sensor	architecture, status register,
			LM35 with Arduino board and	Special function registers,
			display temperature on	SRAM, ROM & EEPROM
			LCD.	space, On-Chip peripherals.
Month : Ar	n:1 2022		Project work	
Lectures	Practicals	Total	Module/Unit:	Sub-units planned
Dectares	racticals	Total	3) AVR programming in C:	3) AVR Data types, AVR
12	40	52	Beet Solice	I/O port programming,
		32	The state of the s	Timer programming, Input
			telephone Telephone	capture and Wave
Control of	PER DEL			Generator, PWM
4 19				programming, External
			Security resultation and Abuse All 1	Interrupt programming,
	300	of distances	TO SECURE THE SECURE OF SCHOOL SECURE	ADC programming, Serial Port programming.
		if South	4) Peripheral interfacing and	4) Interfacing of Switches,
			embedded system:	Relays, LEDs, seven
			stell sectoriomis - 50% (LAME)	segment display 16x2 LCD
and the state of	11-71-2-7		Aleksey and Astrophy	Interfacing, Stepper
				interfacing.
			Practicals:	TERM THE REAL PROPERTY.
			Group D: (AVR and	
Observe of	S Phills-		Instrumentation)	
		14.07	5. Interface temperature sensor	
			Humidity Sensor (DHT11) with	
			Arduino/AVR board and display	
			temperature and humidity values on LCD.	
1000			6. Accelerometer Sensor Interfacing with Arduino/AVR microcontroller.	7. 7.
18	Te	1	7. Study of temperature sensor RTD	CANE
	611		and Thermistor	
Min-in	CH CHE		8. Function generator using IC 8038	COLLEGA
	Head		Project work	De la
				2.2 . 18
	treent of El	Depart		10.6 19

Month: M	ay 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	5) Designing of an Embedded System:	5) DC Motor speed control using PWM technique,
12	40	52	Spannent of Plant State of the Control of Plant State of the Control of the Contr	Measurement of Temperature of an environment using sensor LM35, Dual channel Digital Voltmeter.
Month: Ju	ine 2022		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	
12	40	52	Group D: (AVR and Instrumentation) 9. Automatic Porch light control using LDR and relay. 10. Study of the characteristics of Resistance Temperature Detector (RTD) 11. To study transducer (Thermistor/ Thermocouple) Project work	Marie Janeary 2017

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Department of Electronics Academic Year: 2022-23

Annual Teaching Plan

Name of the teacher: Dr. Milind S. Patil

Programme: B.Sc. III Semester- VI

Subject: Electronics Course Title: SEC 4: Embedded System Design using Arduino

Month: Ja	anuary 2023		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	3) Display Interfacing	Interfacing Arduino to LED's-
6	0	6	micracing	blinking single LED, blinking multiple LED's, 7 segment display
Month: Fe	bruary 2023		Module/Unit:	Sub-units planned
6	0	6	3) Display Interfacing	traffic light, LED flashes, LED dot matrix, Interfacing to LCD's- Basic LCD control, display a message on LCD display
	Month: March 2023		Module/Unit:	Sub-units planned
6 Rolland	O	6	4) Interfacing sensors:	Sensors- Definition, Types. Interfacing Arduino to different sensors- light sensor, temperature sensor, humidity sensor,
Month: Ap			Module/Unit:	Sub-units planned
6	0	6	4) Interfacing sensors:	pressure sensor sound sensor, distance ranging sensor, water/detector sensor, smoke, gas, alcohol sensor, ultrasonic range finder.

Dr. Milind S. Patil

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Dr. C. B. Patil

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage Programme: B.Sc. III Semester- (V) Subject: Electronics Course Title: Practical's

Month: N	ovember 202	2	Module/Unit:	Sub-units planned
Lectures 0	Practical's 25	Total 25	Practicals Group A: 1. To design an Amplitude Modulator using Transistor/ Opamp 2. To study envelope detector for demodulation of AM signal 3. To study FM - Generator / Detector 4. To study AM Transmitter / Receiver 5. To study FM Transmitter / Receiver	
Month: De	ecember 2022		Module/Unit:	Sub-units planned
Lectures 0	Practicals 25	Total 25	Demodulation 6. To study Time Division Multiplexing (TDM) 7. To study Pulse Amplitude Modulation (PAM) 8. To study Pulse Width Modulation (PWM) 9. To study Pulse Position	
Month : Ja	anuary 2022	RIU III	Modulation (PPM) 10. To study ASK modulator Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals Group A:	
0	25	25	11. To study PSK modulator 12. To study FSK modulator Practicals Group B: 1. Addition and subtraction of numbers using direct addressing mode 2. Addition and subtraction of numbers using indirect addressing mode 3. Multiplication by repeated addition 3. Multiplication by repeated addition	

Mr. G. B. Jirage



Dr. C. B. Patil

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage Programme: B.Sc. III Semester-VI

Subject: Electronics Course Title: DSC-1005F1: Industrial Process control and PLC programming

Ionth : N	1arch 2022		Modern	Sub-units planned 1) Significance Transfer Function, Types and
Lectures	Practicals	Total	of control system Unit 2: order of transfer function (Open loop and Close loop transfer system), loop and Close loop transfer system), loop and Close loop transfer system), loop and Close loop transfer system)	
2		12	Control System	reduction rules, Basic elements of control system, open loop controlsystem, closed loop control system, control system terminology, manually controlled closed loop systems, automatic controlled closed loop systems, comparison closed-loop system and open-loop control, feed-forward control system, adaptive control system, classification of control system.ON-OFF controller, proportional control, PI controller, PD controller and PID control. Introduction to Fuzzy Controller 2) Op-amp as a zero crossing detector, non-inverting comparator, inverting comparator, inverting comparator, two position control using opamp, proportional controller, integral controller using Op-amp, derivative controller, PI controller, PID controller.
Month:	April 2022		Module/Unit:	Sub-units planned
Lecture 12	1	s Total	Unit 3 :Introduction to PLC Practicals:	Definition, overview of PLC systems, block diagram of PLC, input/output modules, power supplies, isolators, features like scan time, system scale, user interface. Modular PLC and Redundant PLC and Applications. Industrial Communication Buses: RS485, Profibus .Distributed control system, DCS components/block diagram, SCADA, adaptive control system.
Month	: May 2022			Sub-units planned 4) Basic components: fuse, pushbutton,
	res Practica	Cold Cold	Unit 4 :Ladder	4) Basic components, ruse, pastiouters,



Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage Programme: B.Sc. III Semester- V

Subject: Electronics Course Title: SEC 3:Renewable Energy

Month : No	vember 2021	THE STATE OF	Module/Unit:	Sub-units planned Causes of Energy Scarcity, Solution to Energy
Lectures	Practicals	Total	1)Introduction to Renewable Energy	Scarcity, Factors Affecting Energy Resource Development, Energy Resources and
2	0	12		Classification, Renewable Energy – Worldwide Renewable Energy Availability,
lectronic e, Kolha	Head srbment of E	Dep	2) Solar energy	Renewable Energy in India. Solar energy, its importance, storage of solar energy, solar pond, nonconvective solar pond, applications of solar pond and solar energy, solar water heater, flat plate collector, solar distillation, solar cooker, solar green houses, solar cell, absorption air conditioning. Need and characteristics of photovoltaic (PV) systems, PV models and equivalent circuits, and sun tracking systems
Month: D	December 202	1		I Mind
12	0	12	3) Wind Energy harvesting:	Fundamentals of Wind energy, Wind Turbines and different electrical machines in wind turbines, Power electronic interfaces, and grid Interconnection topologies.
			4) A. Ocean Energy:	Ocean Energy: Ocean Energy Potential against Wind and Solar, Wave Characteristics and Statistics, Wave Energy Devices.
Month:	January 2022			1 D Godhermal
12	0	12	4)B. Geothermal Energy: 4)C. Hydro Energy: 5) Piezoelectric Energy harvesting	Technologies. Hydropower resources, hydropower technologies, environmental impact of hydropower sources: Introduction, Physics and characteristics of piezoelectric effect, materials and mathematical description of piezoelectricity, Piezoelectric parameters and modeling piezoelectric generators,
				Piezoelectric energy harvesting applications, Human power

Mr. G. B. Jirage

Dr. C. B. Patil Head

12	0	12	Programming basics	selector switches, limit switches, Indicators, relay, timedelay relays functions and symbols. General PLC Programming procedures, programming on-off inputs/ outputs. Auxiliary commands and functions: PLC Basic Functions: Register basics, timer Functions, counter functions. Ladder Programming: Programs for Boolean logic and flip-flops, counters, timers, flasher. Application program Bottle filling plant, elevator control, washing
	- varsod s	desco		machine control.

Mr. G. B. Jirage



Dr. C. R. Patil

Head

Department of Electronics

Vivekanand College, Kolhapus.

Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: **Mr. G. B. Jirage** Programme: B.Sc. I Semester- II

Subject: Electronics Course: DSC-1005B Section-I Analog Electronics-II

Mon	th: March 20	122	Module/Unit:	Sub-units planned BJT: Structure, Working	
Lectures Practicals Total 8 32 40		Total	Unit 1: Bipolar Junction Transistor: Practical: Group- B 1. Study of Half Adder and Full Adder 2. Study of Multiplexer (4:1) and Demultiplexer (1:4) 3. Study of I-V Characteristics of JFET. 4. Study of Input and Output Characteristics of CE configuration of BJT	of transistor. Transistor configurations: CB, CE and CC, characteristics of transistor in CE and CB configurations, Current gains α and β, dc load line and Q point	
			2) Unipolar Devices:	JFET: Construction, working and I-V characteristics MOSFET: Construction, working and I-V characteristics, UJT: introduction, structure and characteristics.	
Mo	nth : April 20		Module/Unit:	Sub-units planned	
Lectures 8	Practical 32	Total 40	 3) Amplifiers: Practical: 5. Wein Bridge Oscillator. 6. Study of the Colpitt's oscillator 7. Study of the Hartley oscillator. 8. Building and testing of RS Flip-Flop using NAND/NOR gate 	Transistor biasing Fixed Bias and Voltage Divider Bias, Class A, B AB and C Amplifiers Single stage CE amplifier, Cascaded Amplifiers: Two stage RC, LC, TC and DC Coupled Amplifier ,Concept of Differential amplifier and it advantages	
Mo	Month: May 2022		Module/Unit:	Sub-units planned	
Lectures		Total	4) Feedback Amplifier and Oscillators:	Negative and positive feedback, Oscillators:	



8 32	40	Barkhausen criterion for sustained oscillations, Phase shift, Wein Bridge, Hartley and Colpitt's oscillator,UJT as relaxation oscillator
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Mr. G. B Jirage

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Department of Electronics

Vivekanand College, Kolhapur.



Department of Electronics Academic Year: 2021-22

Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage Programme: B.Sc. I Semester- I

Subject: Electronics Course Title: DSC-1005A Section-I Analog Electronics-I

	ovember 20		Module/Unit:	Sub-units planned
Lectures 8	Practicals 32	Total 40	1) Basic Circuit Elements Practical's: Group- A 1. To familiarize with basic electronic components (R, C, L, diodes, transistors), Digital Multimeter, Function Generator, power supplies and Oscilloscope etc. 2. Measurement of Amplitude, Frequency & Phase difference using Oscilloscope. 3. Verification of Thevenin's Theorem.	Study of basic circuit elements and passive components: Resistor, Capacitor, Inductor, Transformer, Relays, Switches (working principle, circuit symbols, types, specifications and applications).
	3 to them	Desar	Verification of Norton's Theorem.	The second secon
Month: De	cember 202	2naxavi	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	2) Circuit Analysis Practicals: 4. Verification of Superposition Theorem.	Concept of Voltage and Current Sources, Internal resistance,
8	32	40	5. Study of the I-V Characteristics of P-N junction Diodes. 6. Study of the breakdown Characteristics of Zener Diode 7. Study of Half wave rectifier	Kirchhoff's Current Law, Kirchhoff's Voltage Law. Mesh Analysis. Node Analysis. Principle of Duality, Superposition Theorem, Thevenin's Theorem, Norton's Theorem, Maximum Power Transfer Theorem, Millman's Theorem. (Numericals expected)
			3) PN Junction Diode	Construction of PN junction, Formation of Depletion Layer, Barrier potential, Forward and Reverse bias, Diode Equation and I-V characteristics, Zener diode, Zener and Avalanche breakdown, Zener diode specifications. Photo diode. Light Emitting Diode (LED): construction and working, 7-segment display and it's applications.
on Gue			HANAND CO	

Month : Ja	Month: January 2022		Module/Unit:	Sub-units planned Need of Power Supply, Block
Lectures 8	Practicals 32	Total 40	4) DC Power Supply Practicals: 8. Study of Full wave rectifier 9. Study of Logic Gates. 10. Study of Universal Gates	diagram of DC regulated power supply, Rectifiers: Half wave, Full wave rectifiers (centre tapped and bridge):- Circuit diagrams,
Manual Ma		Gr.	using fundamental gates. 11. Study of De-Morgans Theorems	working and waveforms, ripple factor, PIV, efficiency and TUF. Filter-Shunt capacitor filter, Series inductor filter, π - filter. Regulation: Concept of Line and load regulation, Zener diode as voltage regulator, Three pin IC regulators: Block diagram, Specifications and applications. Fixed and Variable voltage IC regulator (IC 78xx,79xx and LM317). Concept of SMPS.

Mr. G. B. Jirage

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Department of Electronics
Vivekanand College, Kolhapur.



Department of Electronics Academic Year: 2021-22 Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage Programme: B.Sc. III Semester- V

Subject: Electronics Course Title: SEC 3:Renewable Energy

Month : A	ugust 2021		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	1)Introduction to Renewable Energy	Causes of Energy Scarcity, Solution to Energy Scarcity, Factors Affecting Energy Resource Development, Energy Resources and
2 0	0	0 12	A CONTROL AS	Classification, Renewable Energy – Worldwide Renewable Energy Availability, Renewable Energy in India.
			2) Solar energy	Solar energy, its importance, storage of solar energy, solar pond, nonconvective solar pond, applications of solar pond and solar energy, solar water heater, flat plate collector, solar distillation, solar cooker, solar green houses, solar cell, absorption air conditioning. Need and characteristics of photovoltaic (PV) systems, PV models and equivalent circuits, and sun tracking systems
LI WILLIAM STATE OF THE PARTY O	eptember 2021		63 117 1 1	Find an artale of Wind anaray Wind
	0 Head Intent of El		3) Wind Energy harvesting:	Fundamentals of Wind energy, Wind Turbines and different electrical machines in wind turbines, Power electronic interfaces, and grid interconnection topologies. Ocean Energy: Ocean Energy Potential against Wind and Solar, Wave Characteristics and Statistics, Wave
			4) A. Ocean Energy:	Energy Devices.
A SHARE ASSESSMENT AND REAL PROPERTY.	October 2022		WD 6 1 1	C. d. and Bassyman Gaethermal
12	0	12	4)B, Geothermal Energy: 4)C. Hydro Energy:	Geothermal Resources, Geothermal Technologies. Hydropower resources, hydropower technologies, environmental impact of hydro power sources:
Month:	Nov 2022			N 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
12	0	12	4 5) Piezoelectric Energy harvesting:	Introduction, Physics and characteristics of piezoelectric effect, materials and mathematical description of piezoelectricity, Piezoelectric parameters and modeling piezoelectric generators, Piezoelectric energy harvesting applications, Human power



Department of Electronics

Vivekanand College, Kolhapur.

12	0	12	Programming basics	selector switches, limit switches, indicators, relay, timedelay relays functions and symbols. General PLC programming procedures, programming on-off inputs/ outputs.
Month: A	April 2023			that believed the same and the little like
Lectures	Practicals	Total	and te reserve	Auxiliary
12	0	12	Unit 4 :Ladder Programming basics	commands and functions: PLC Basic Functions: Register basics, timer functions, counter functions. Ladder .
Month: n	nay 2023		e area was	
Lectures	Practicals	Total	Let , risking the size of	Programming: Programs for Boolean logic and flip-flops, counters, timers, flasher.
12	0	12	onto jose injektos Justoppkosty i s Ma stolestypo	Application program Bottle filling plant, elevator control, washing machine control

Mr. G. B. Jirage

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Dr. C. B. Patil

Head

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