

# Vivekanand College, Kolhapur ( Autonomous)

Department of Electronics

B. Sc-II, Sem-IV


Paper Code: (DSC 1005D)

## Section I & II: Advance Communication & Microcontroller 8051 Internal Examination Notice

Date: 17/07/2021

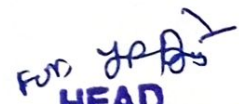
This is to inform all the student of B. Sc.-II class, we are conducting **internal examination** for 20 marks on **23/07/2021** using **Google platform**. The Google form link will be sent on what's app group 5 min before commencement of the exam. The detail time schedule of online test is given below.

Date	Time	Mode
23/07/2021	11.00 am-12.00 pm	Online-Google Platform

  
Subject teacher

Mr. N.P.Mote



  
**HEAD**  
DEPARTMENT OF ELECTRONICS  
VIVEKANAND COLLEGE, KOLHAPUR  
(AUTONOMOUS)

# Vivekanand College, Kolhapur

## (Autonomous) B.Sc-II (Sem-IV)

### Internal Exam-July-2021 Electronics

Paper No-IV

Section-I& II

Subject Code: DSC-1005D

Name of Paper: Advance Communication and Microcontroller 8051

Date: 23 /07/2021

Time:11.00 am to 12.00 am

Total Marks: 20

Instructions:

- 1.All Questions are compulsory.
- 2.Each Question carries 1 Marks.
- 3.Figure to write indicate full marks.

\* Indicates required question

1. Email \*

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2. Student name ( First name\_ Middle name\_ Last name) \*

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3. 1. Amplitude of pulse train varies according to instantaneous value of modulating signal is called ---

*Mark only one oval.*

PWM

PAM

PCM

PDM



4. 2. Which is correct for Nyquist rate, if  $f_s$  = frequency of sampling,  $f_m$  = highest frequency of band limited signal

Mark only one oval.

- $f_s = f_m$   
  $f_s > 2f_m$   
  $f_m > 2f_s$   
  $f_s < 2f_m$

5. 3. PAM stands for-----

Mark only one oval.

- Phase Amplitude Modulation  
 Pulse Amplitude Modulation  
 Phase Addition Modulation  
 Pulse Addition Modulation

6. 4. Which is not a type of pulse modulation?

Mark only one oval.

- Pulse frequency Modulation(PFM)  
 Pulse Position Modulation(PPM)  
 Pulse Amplitude Modulation(PAM)  
 Pulse Pulse Modulation(PWM)

7. 5. How many voltage levels are present in a PWM signal?

Mark only one oval.

- 1  
 2  
 3  
 4



8. 6. Which of the following requires a synchronizing signal ?

*Mark only one oval.*

- PAM
- PWM
- PPM
- All of these

9. 7. In -----, the amplitude and width of pulse are constant.

*Mark only one oval.*

- PAM
- PWM
- PPM
- All of these

10. 8. The sharing of a medium and its link by two or more devices is called .....

*Mark only one oval.*

- Modulation
- Duplexing
- Multiplexing
- All of these

11. 9. ----- multiplexing technique that shifts each signal to a different carrier frequency.

*Mark only one oval.*

- TDM
- FDM
- Both TDM and FDM
- CDM



12. 10. ----- is the type of analog pulse modulation systems.

*Mark only one oval.*

- FM
- AM
- PCM
- PAM

13. 11. The 8051 microcontroller has total \_\_\_\_\_ I/O pins.

*Mark only one oval.*

- 8
- 16
- 32
- 64

14. 12. The 8051 microcontroller has \_\_\_\_\_ bytes of on chip RAM.

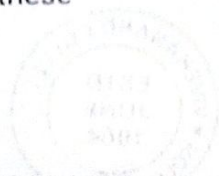
*Mark only one oval.*

- 128
- 256
- 512
- 1028

15. 13. The address bus of 8051 microcontroller is \_\_\_\_\_ bit.

*Mark only one oval.*

- 8
- 16
- 32
- None of these



16. 14. The 8051 microcontroller has \_\_\_\_\_ bytes of on chip program memory.

*Mark only one oval.*

- 1K
- 2K
- 4K
- 8K

17. 15. The 8051 microcontroller has \_\_\_\_\_ 16 bit timer/counters.

*Mark only one oval.*

- 1
- 2
- 3
- 4

18. 16. In 8051, the stack pointer register is \_\_\_\_\_ bit register.

*Mark only one oval.*

- 8
- 16
- 32
- 64

19. 17. On reset, the default address in stack pointer is \_\_\_\_\_.

*Mark only one oval.*

- 00H
- 07H
- FFH
- 08H



20. 18. The total number of register banks in 8051 microcontroller are \_\_\_\_\_.

*Mark only one oval.*

1

2

3

4

21. 19. A logic \_\_\_\_\_ on RST pin resets the 8051 microcontroller.

*Mark only one oval.*

Low

High

high impedance

none of these

22. 20. In 8051 RAM space, bit addressable area starts from \_\_\_\_\_.

*Mark only one oval.*

10H to 1FH

20H to 2FH

30H to 3FH

40H to 4FH

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Google Forms



**Vivekanand College, Kolhapur (Autonomous)**

B.Sc.-II (Sem-III)

Paper Code: DSC 1005C

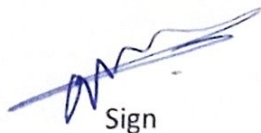
Paper Name : Advance Communication and Microcontroller 8051

Sr.No	Timestamp	Student name	Marks Outoff 20
1	23-07-2021 11:17	Vaishnavi Shashikant Gove	10
2	23-07-2021 11:21	Aadesh satish patil	18
3	23-07-2021 11:24	Siddhanath chandrakant gopane	15
4	23-07-2021 11:24	Ketan Dattatray Khanaj	14
5	23-07-2021 11:24	Vaibhav Maruti Kadvekar	15
6	23-07-2021 11:25	Abhishek Sharad Potdar	18
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9	23-07-2021 11:28	Sanket Sarjerao Ekal	15
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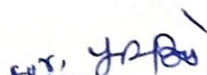


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51	23-07-2021 11:44	Shreya Rahul Korde	17
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56	23-07-2021 11:46	Aishwarya Sunil Chavan	20
57	23-07-2021 11:46	Shreya shahaji patil	16
58	23-07-2021 11:47	Deepa Anil Dhamanekar	18
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65	23-07-2021 11:50	Pranjal Prakash Shintre	18
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80	24-07-2021 12:31	ASITKUMAR UTTAM PATIL	16
81	31-08-2021 13:56	Abhishek Ajit Patil	17
82	22-09-2021 13:27	Akshad Mahesh Pawar	16

  
Sign

Subject teacher

(Mrs N.P. mote)

  
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