Department of Electronics Academic Year: 2023-24 **Annual Teaching Plan**

Name of the teacher: Dr. C. B. Patil

Programme: B.Sc. II Semester- III
Subject: Electronics Course Title: DSC-1005C Section-II Microprocessor 8085

	Subject: Electronics Course Title: DSC-1005C Section-II Microprocessor 8085					
Month: J	uly 2023		1110	Sub-units planned		
Lectures	Practicals	Total	Unit-1: Microcomputer Organization:	Components of microcomputer, RAM (SDRAM, DRAM), ROM		
6	16	22		Memory Man		
			N. 1.1 /77 '.	Memory Map		
Month:	August 2023		Module/Unit:	Sub-units planned		
Lectures	Practicals	Total	Unit-1: Architecture of 8085 Microprocessor:	features of 8085.Block diagram and Pin description of 8085.		
12	32	44	Practicals Group A:	Data and address bus, Registers,		
12	32		1.To study Amplitude Modulator	ALU, Stack pointer, Program		
			and demodulator	counter, Flag register, Clock and		
			2. To study FM modulator	reset circuits. Interrupts in		
			3. Pulse Amplitude Modulation	8085.Demultiplexing of AD0-		
			(PAM)	AD7.T-states, Machine cycle,		
			4.To study Pulse Width Modulation	Instruction cycle. Timing		
				diagram of MOV and MVI		
			5.To study ASK Modulator	instructions		
Month: S	Sept 2023		Module/Unit:	Sub-units planned		
	Practicals	Total	Unit-2: Instruction Set of 8085	classification of Instruction Set,		
Lectures			Microprocessor:			
12	32	44		Addressing modes, Instruction		
			Practicals Group A:	set: Data transfer, Arithmetic,		
			6. To study PSK Modulator	logical, branch and control		
			7. To study FSK Modulator	instructions		
			8. To study PCM			
	1		9. To study PPM			
			10. Study of Tuned Amplifier			
	1		Practicals Group C:			
			1. Addition of Two 8 Bit Numbers			
			2. Subtraction of Two 8 Bit Numbers			
			3. Multiplication of Two 8 Bit Num.			
Month:	Oct 2023		Module/Unit:	Sub-units planned		
Lectures	Practicals	Total	Unit-3: Programming with 8085	Programs of Addition (8 and 16		
10	22	11	Microprocessor:	bit), Subtraction, Multiplication,		
12	32	44	Practicals Group C:	Division, Block Transfer and		
			4. Division of Two 8 Bit Numbers	Exchange, Masking, ascending		
			5. Program to transfer the memory	and descending order,		
			block using 8085			
	100					
Month : Nov 2023			6. Exchange the memory blocks Module/Unit:			
			Unit 4. Decommend 11 0005	Sub-units planned		
Lectures	Practicals	Total	Unit-4: Programming with 8085 Microprocessor:	Time delay generation using		
4	12	16	7. Ascending and descending	register and register pair,		
			7. Ascending and descending	Detection of odd and even		
			8. To find even and odd numbers	numbers.		
			using 8085			
			9. To find total number of even and			
			10.Odd numbers in an array using			
	1 37817		8085			
ji.		100	11. Programs for masking and to find			
14	(A)		parity of given number	1,7		
	DV		COLLEG			

HEAD

DEPARTMENT OF ELECTRONICS

Vivekanand College, Kolhapur (Autonomous) Department of Electronics

Academic Year: 2023-24 **Annual Teaching Plan**

Name of the teacher: Dr. C. B. Patil Programme: B.Sc. II Semester- IV

05D Section-II Microcontroller 8051

Programme: B.Sc. II Semester-TV Subject: Electronics Course Title: DSC-1005D Section-II Microcontroller 8051 Subject: Electronics Course Title: DSC-1005D Section-II Microcontroller 8051				
Subject: E	lectronics	Course 11	Module/Unit:	Sub-units planned
Month: D		Total	Unit-1: Introduction to	features of 8051 family, , Block
Lectures	Practicals		8051microcontroller:	diagram of 8051,
3	8	11		Sub-units planned
Month: Ja	an 2024		Module/Unit:	Pin description of 8051, RAM
Lectures	Practicals	Total	Unit-1: Introduction to	structure of 8051, SFR's and
			8051 microcontroller:	CDD's in 8051 PSW register
			Practical's Group B (Op-Amp):	Clock and reset circuit, Memory
			1. To design Op-Amp as Inverting	organization ,I/O Ports.
			and Non-Inverting amplifier	
			2.Op-Amp as adder and Subtractor	
				·
10	32	44	3.Op-Amp as integrator and	
12	32	44	differentiator	
			4. To study Op-Amp as Schmitt	
			trigger.	
			5. To study Op-Amp as comparator	
			6. To design phase shift oscillator	
			using Op-Amp	Sub-units planned
Month: F	eb 2024		Module/Unit: Unit-2: Instruction Set of 8051:	Classification of instruction sets,
Lectures	Practicals	Total	Practical's Group B (Op-Amp):	Addressing modes.
				Instruction set of 8051: Data
			7.To design Wein bridge oscillator	transfer, Arithmetic, Logical,
			using Op-Amp	Jump, Call,
			8. To study Op-Amp as triangular	Boolean instructions.
		١	wave generator	
12	32	44	9. Op-Amp as Square wave	
			generator	
			10. Op-Amp as precision rectifier.	
			11. Op-Amp as peak detector	
Month: N	March 2024		Module/Unit:	Sub-units planned
		m	Unit-3: 8051Timer Programming:	Introduction to Timers, Timer
Lectures	Practicals	Total	Practical's Group D:	Registers, Timer modes and
			1. Arithmetic instruction	Timer Programming using mode
			programming	1 and mode 2:- Square wave
				generation,
			2. Logical instruction programming	rectangular wave generation
			3.Bit manipulation instruction	Counter Programming: pulse
			4.Code conversion using 8051	counter counter
			5. Timers of 8051 in mode 1	
12	32	44	6. Study of timers of 8051 in	
			mode 2	
				COLLEGE
			/	Children Children
,			/s	F/ ESTD \2\
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				The state of the s

Month : A	pril 2024		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit-4: 8051 Serial and Interrupt Programming:	Serial ports: Serial port of 8051, modes, Serial port Registers,
12	32	44	Practical's Group D: 7. Study of counters of 8051 8. Study of Serial programming 9. Study of Timer Interrupts programming of 8051 10. Study of Serial communication Interrupts programming of 8051 11. Study of External hardware Interrupts programming of 8051	Serial port programming. Interrupt: Interrupt in 8051, Interrupt registers, Programming with interrupt

Dr. C. B. Patil



Dr. C. B. Patil

HEAD

DEPARTMENT OF ELECTRONICS

VIVEKANAND COLLEGE, KOLHAPUR

(EMPOWERED AUTONOMOUS)

Department of Electronic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Dr. C. B. Patil Programme: B.Sc. III Semester-V

Course Title: DSC-1005E1 Section – II 8051 Microcontroller Interfacing

			Module/Unit:	Sub-units planned
Month	Month: July 2023		Module/Unit: Unit-1: Introduction to Embedded C	Introduction to Embedded C Advantages and
Lecture	es Practicals	Total	Ome-1. Indoos	disadvantages of
		15	-	programming in 8051-C &
10	5	13		Assembly Language.
				Data types,
				Sub-units planned
Month:	August 2023		Module/Unit:	Time delay – using for loop
Lecture	s Practicals	Total	GROUI A .(Instrumentation	and using 8051 Timers, I/O programming,
12	20	32	1. Design of multi-range ammeter,	Logical operations, Data
S NOTH BU	DEPAREMENT		voltmeter, conversion of ammeter	conversion programs
and 1 41 7 112			voltmeter	Conversion program
residentile d	Ethio a dan		2. Study of temperature sensor RTD	
			3. Automatic Porch light control using	
			LDR and relay	
			4. Measurement of displacement -	
			LVDT	
			5. ON/OFF Temperature controller	
Month: S	Sept 2023		Module/Unit:	Sub-units planned
Lectures		Total	Unit-2: Interfacing of Input Output	LED, Relay, Opto-coupler,
12	20	32	Devices	LCD, Seven Segment
1		32	Output devices::	Display, Seven
			GROUP A:(Instrumentation-I & II)	Segment Display
			1. Study of solid state relay	(multiplexing mode), DC
			2. Function generator using IC 8038	Motor, Stepper Motor
			3. Instrumentation amplifier	Input devices: Switch, 4X4
			4. Active filter: Low and High Pass	matrix keyboard, thumb
			5. Study of active filter: Band Pass	wheel switch
			Study of V to F and F to V using VCO	
Month : C	Oct 2023		Module/Unit:	Sub units also
			Unit-3: ADC, DAC Interfacing:	Sub-units planned
			,	Interface ADC 0804, ADC
Lectures	Practicals	Total		0808/0809, ADC
				MAX1112, DAC 0808 (Triangular
- 10				wave, Ramp, Staircase)
12	20	32	Unit-4: Sensor Interfacing	Reed sensor, smoke sensor,
			GROUP B: (Antenna)	PIR sensor, Temperature
			1. Study of simple dipole $\lambda/2$ antenna	sensor (LM 35, PT-100),
			2. Study of folded dipole $\lambda/2$ antenna	Humidity sensor (SY HS
			3. Study of simple dipole $\lambda/4$ antenna	230), Light sensor (LDR),
			4. Study of Yagi-Uda with 3 and 5	Moisture/rain sensor, Gas
			element simple discala	sensor (MQ series),
			element simple dipole antenna	, , , , , , , , , , , , , , , , , , , ,
			Project work	
			CULLEGA	
			NO CE TO	
Month : No	ov 2023		Module/Unit: ESTD	

Lectures	Practicals	Total	Unit-4: Sensor Interfacing GROUP B: (Power Electronics)	AC current sensor (CT-current Transformer), AC
12	5	17	 SCR firing by UJT AC Voltage controller Speed Control of DC Motor. Phase Shift control of SCR Design of Single phase full wave controlled rectifier To study the simulation of single phase HWCW with R & RL load using Project work 	voltage sensor (PT-potential transformer), LVDT, Ultrasonic module

Dr. C. B. Patil



Dr. C. B. Patil

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DEPARTMENT OF ELECTRONICS
VIVEKANAND COLLEGE, KOLHAPUR
(EMPOWERED AUTONOMOUS)





Academic Year: 2023-24 **Annual Teaching Plan**

Name of the teacher: Dr. C. B. Patil Programme: B.Sc. III Semester-VI

Subject: Electronics Course Title: Practicals

	gramme: B.d ject: Electron		urse Title: Practicals	Sub-unit
	Month: January 2024		Module/Unit:	planned
			Unit 1:Power semiconductor devices	
Lectur	res Practica	ls Tota	GROUP C: (µC 8051 & PLC)	
			Timers in 8051 IIC.	
0	40	40	2 LED. Switch and Relay interfacing to 3031 per	
			3 I CD Interfacing with 8051 μC	1
			4. 4.DC motor interfacing to 8051	
			5. Stepper Motor interfacing to 8051	
			 6. DAC0808 interfacing to 8051 7. ADC0804 interfacing to 8051μC 	
			7. ADC0804 intertacing to 55 7	Sub-units
	20	224	Module/Unit:	planned
Month	: February 20)24	Module, Olit.	planned
	15	Tetal	Unit 2: Thyristors	
Lecture			CROUP C: (IIC 8051 & PLC)	
0	40	40	8. Serial communication with PC using 8051 μC.	
			a Study of DI C Simulator	
			9. Study of FEC Simulator 10. Programming with PLC for sequential logic RS-FF, JK-FF	
			11. Programming with PLC for sequential logic T-FF,D-FF	
			12. Study of PLC timers and counters	1
			and the state of t	Sub-units
Month:	March 2024	1	Module/Unit:	planned
		1	Unit 3: Controlled Rectifiers	
Lectures	Practicals		Practicals: GROUP D: (AVR & IoT)	
0	40	40	13. Interfacing of Switches and LED with Arduino.	
			14. LCD Interfacing with Arduino	
			15. Stepper Motor Interfacing with Arduino	-
			16. Interface temperature sensor LM35 with Arduino board	• 9 7
			17. Interface temp. sensor & Humidity Sensor (DHT11) with Arduino	
			18. Accelerometer Sensor Interfacing with Arduino.	
			19. Study the fundamental of IoT Architecture, Arduino and	
			necessary software	
Camth . A	:1 2024		Module/Unit:	Sub-units
ionin : A	pril 2024		Module/Oilit.	planned
				planned
ectures	Practicals	Total		
0	40	40	Unit 4: Power Systems	
COL	EGE + OUHAPUR*/		Practicals:	
AND	TOE TO		GROUP D: (AVR and IoT)	
EST	D / []		11.Interface Bluetooth with Arduino & send the sensor data to	
JUK	E S		smartphone through Bluetooth	1
196	4 3		12.Interface Bluetooth with Arduino and receive the data from	ı
	utonomous		smartphone through Bluetooth to turn LED ON/OFF	
Wered A	utono		13.Interface Wi-Fi module with Arduino to upload sensor data.	

14.Interface GSM module with Arduino to upload sensor data 15. Study and implement MQTT protocol using Arduino 16.IoT Application Case study: Home Automation
Project work

Dr. C. B. Patil



Dr. C. B. Patil
HEAD
DEPARTMENT OF ELECTRONICS
VIVEKANAND COLLEGE, KOLHAPUR
(EMPOWERED AUTONOMOUS)

Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. I Semester- I

Subject: Electronics IKS: IKS03GEC11: Indian Knowledge System

Month:	Module/Unit:	Sub-units planned
August 2023		A 11-4-
Lectures: 12	Unit 1: Indian Astronomy	Ancient Indian Astronomy, Aryabhata, Astronomers after Aryabhata, Celestial Horizon, Meridian, Pole star and directions, Zodiac and constellations, Zodiac and Rashis, Naksatra system, Civil day and Sideral day, Solar year and Civil calendar, Solar month and lunar month, Yuga system, Indian Eras, Cause of Lunar eclipse, angular diameter of the shadow-cone, Cause of solar eclipse, Angular distance between the sun and the moon at the beginning and end of a solar eclipse.
Month:	Module/Unit:	Sub-units planned
September 2023		Nbox
Lectures: 12	Unit 2: Maths in Ancient and Medieval India	Unique Aspects of Indian Mathematics, Number System in India-Historical Evidence, Salient Features of the Indian Numeral System, Concept of Zero and it's Importance, PINGALA and the binary system with Combinatorial Problems in Chandah-Sastra of PINGALA, Great Mathematicians and their Contributions, Property of Right Angle Triangle in Shlba-sutras, The value of π , Magic Square in India
Month:	Module/Unit:	Sub-units planned
October 2023		Introduction, Chemistry in pre-Harrapan period,
Lectures: 12	Unit 3: Chemistry in Ancient and Medieval India	Chemistry in Indus Valley civilization, Chemistry in post-Harrapan period, Chemistry in Vedic and Ayurvedic: Chemistry in Charaka and Sushruta, Chemistry in Tantric Period: Rasaratnakara of Nagarjuna, Iatrochemical period.
Month:	Module/Unit:	Sub-units planned
November 2023		Distribution of
Lectures: 6	Unit 4: Introduction to Ayurveda.	Ayurvedic medicinal system, Principles of Ayurveda (Vata, Pitta, Kapha), Ayurvedic therapies, Unani system of medicine, Siddha system of medicine, Future of herbal drugs, Study of medicinal plants Punarnava, Vasaka, Shatavar, Brahmi and Arjuna

Mr. P. R. Bagade



Dr. C. B. Patil

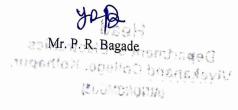
Head
Department of Electronics
Vivekanand College, Kolhapur.
(AUTONOMOUS)

Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. II Semester- III

Subject: Electronics Course Title: DSC-1005C Section-I Electronics Communication

Subject	: Electronics	Cou	rse Title: DSC-1005C Section-1 Elec	C. L
Month:	August 2023		Module/Unit:	Sub-units planned Introduction to communication –
Lectures	Practicals	Total	Unit 1:Electronic Communication Practicals Group A:	means and modes, Block diagram of an electronic communication system,
12	16	28	 To study Amplitude Modulator and demodulator To study FM modulator Pulse Amplitude Modulation (PAM) To study Pulse Width Modulation To study ASK Modulator 	spectrum, band designations and usage, Concepts of bandwidth, gain, attenuation, Channels and base-band signals, Concept of Noise, signal-to-noise (S/N) ratio.
Month: S	eptember 20	23	Module/Unit:	Sub-units planned
Lectures 12	Practicals	Total 28	Unit 2: Analog Modulation- Demodulation Practicals Group A:	Need, Amplitude Modulation, Mathematical expression, modulation index, frequency spectrum and AM power Classification of AM,
			 To study PSK Modulator To study FSK Modulator To study PCM To study PPM Study of Tuned Amplifier Practicals Group B: Addition of Two 8 Bit Numbers Subtraction of Two 8 Bit Numbers Multiplication of Two 8 Bit Num. 	Concept of DSB, SSB generation, Amplitude Demodulation, Phase Modulation (concept only),FM: modulation index & frequency spectrum, equivalence between FM and AM, Generation of FM using VCO, Slope detector, FM Super heterodyne radio receiver
Month : C	October 2023		Module/Unit:	Sub-units planned
Lectures 12	Practicals 16	Total 28	Unit 3: Analog Pulse Modulation Practicals Group B: 4. Division of Two 8 Bit Numbers	Unit 3: Channel capacity, Sampling theorem, PAM, PWM, & PPM.
			5. Program to transfer the memory block using 8085 6. Exchange the memory blocks	Modulation and detection technique for PAM.
	ovember 202		Module/Unit:	Sub-units planned
		Total	Unit 4: Digital Pulse Modulation:	Need for digital transmission, PCM:
12	16	28	 7. Ascending and descending 8. To find even and odd numbers using 8085 9. To find total number of even and 10.Odd numbers in an array using 8085 	Sampling, Quantization and Encoding. ASK, FSK, PSK. Phase Shift Keying (BPSK and QPSK)
	- 1 y 1		 Programs for masking and to find parity of given number 	







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Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

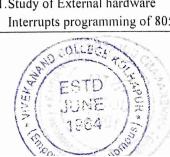
Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. II Semester- IV

Subject: Electronics Course Title: DSC-1005D Section-I Operational Amplifier

Subject	Electronics	Course	Title: DSC-1005D Section-1 Operation	Sub-units planned
Month : J	anuary 2024		Module/Unit:	Transistor de amplifier, Emitter
	T 4 1		Unit 1: Operational Amplifier	coupled Differential amplifier,
Lectures	Practicals		Practical's Group B (Op-Amp):	parameters of Differential
12	16	28	To design Op-Amp as Inverting and	amplifier (Ad, Ac, and CMRR), &
			Non-Inverting amplifier 2. Op-Amp as adder and Subtractor.	configurations of differential
			 Op-Amp as adder and Subtractor. Op-Amp as integrator and 	amplifier. Introduction to op-amp,
			differentiator	block diagram of op-amp,
			4. To study Op-Amp as Schmitt	electrical parameters of op-amp,
			trigger.	offset balancing technique of op-
			5. To study Op-Amp as comparator	amp, study of IC 741.
			6. To design phase shift oscillator	
			using Op-Amp	Colonite planned
Month: Fo	ebruary 2024		Module/Unit:	Sub-units planned Virtual ground concept, Linear
Lectures	Practicals	Total	Unit 2: Applications of Op-amp	Applications: Op-amp as inverting
12	16	28	Practical's Group B (Op-Amp): 7. To design Wein bridge oscillator	and non- inverting amplifier,
			7. To design Wein pridge oscillator using Op-Amp	Voltage follower, Op-amp as
			8. To study Op-Amp as triangular	adder and Subtractor,
			wave generator	Non-Linear Applications:
			9. Op-Amp as Square wave generator	Differentiator and Integrator
			10. Op-Amp as precision rectifier.	
			11. Op-Amp as peak detector	
Month : N	1arch 2024		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 3: Oscillators	Unit 3: Phase shift oscillator, Wien -bridge oscillator,
12	16	28	Practical's Group D:	Wien -bridge oscillator, Triangular wave generator, Square
			Arithmetic instruction programming Logical instruction programming	wave generator, Saw tooth wave
			Logical instruction programming Bit manipulation instruction	generator.
			4. Code conversion using 8051	8
			5. Timers of 8051 in mode 1	
			6. Study of timers of 8051 in mode 2	
Month : A	pril 2024		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 4: Comparators and Rectifiers	Basic comparator, Zero crossing
12	16	28	Practical's Group D:	detector, Schmitt trigger ,Peak
12	10	20	7. Study of counters of 8051	detector, Clippers (positive and
			8. Study of Serial programming	negative) and Clampers (positive
			9. Study of Timer Interrupts	and negative)
			programming of 8051 10.Study of Serial communication	Precision rectifiers: Op-amp as precision rectifiers
			Interrupts programming of 8051	precision rectificis
			11.Study of External hardware	
			Interrupts programming of 8051	^ ~
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Mr. P.R. Bagade

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Dr. C. B. Patil Head Department of Electronics Vivekanand College, Kolhapu

Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade

Subject: Electronics Course Title: DSE-1005E2 Section - II Antenna and Wave Propagation

Subje	ect: Electroni	ÇS C	ourse Trees	Sub-units planned
Month : A	Month: August 2023		Module/Unit:	Antenna as an element of wireless
		Tatal	Unit 1: Antenna Theory	communication system, Antenna
Lectures	Practicals	Total	GROUP A :(Instrumentation-I & II)	radiation mechanism, current
12	40	52	1. Design of multi-range ammeter,	distribution on thin wire antenna.
			voltmeter, conversion of ammeter	Types of Antennas, Fundamentals
			voltmeter	of EMFT: Maxwell's equations and
			2. Study of temperature sensor RTD	
			3. Automatic Porch light control using	their applications to antennas.
			LDR and relay	
			4. Measurement of displacement -LVDT	
			5. ON/OFF Temperature controller	
Month: So	eptember 202	23	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 2: Antenna Parameters	Radiation pattern, Main Lobe and
12	40	52	GROUP A:(Instrumentation-I & II)	Side Lobes, Half-power beam
			6. Study of solid state relay	width, Radiation intensity, Antenna
			7. Function generator using IC 8038	efficiency, Directivity, Gain,
			8. Instrumentation amplifier	effective area, effective length,
			9. Active filter: Low and High Pass	Bandwidth, Polarization, input
			10.Study of active filter: Band Pass	impedance, radiation eff.
) f			11.Study of V to F and F to V using VCO	·
Month : (October 2023		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 3: Radiating wire Structures	Monopole, Dipole, Folded dipole,
12	40	52	GROUP B: (Antenna)	Yagi-Uda, Loop & Bi-conical
			12. Study of simple dipole λ/2 antenna	broadband Antenna, Microstrip
			13. Study of folded dipole λ/2 antenna	Antennas: Basics and its
			14. Study of simple dipole \(\nabla\)4 antenna	characteristics, feeding methods,
			15.Study of Yagi-Uda with 3 and 5 element	design of rectangular, Concept of
			simple dipole antenna	smart antenna: Concept, benefits,
			Project work	Fixed weight & Adaptive
Month · N	Vovember 20	122	Madala (III.)	beamforming basics.
		723	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 4: Radio Wave Propagation	Different Modes of Wave
12	40	52	GROUP B: (Power Electronics)	Propagation, Structure of
			16. SCR firing by UJT	atmosphere, Ground wave, Space
			17. AC Voltage controller	Wave propagation. Sky Wave
			18. Speed Control of DC Motor.	Propagation - Introduction,
			19. Phase Shift control of SCR	Structure of Ionosphere, Refraction
			20. Design of Single phase full wave	and Reflection of Sky Waves by
			controlled rectifier	Ionosphere, Ray Path, Critical
			21. To study the simulation of single phase	Frequency, MUF, Virtual Height
			HWCW with R & RL load using	and Skip Distance, Relation
			Project work	between MUF and skip Distance,
				Multi-hop Propagation.

Mr. P. R. Bagade



Dr. C. B. Patil

HEAD DEPARTMENT OF ELECTRONICS "VEKANAND COLLEGE, KO: 'APIIR IN'TONONE

Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Mr. P. R. Bagade Programme: B.Sc. III Semester- VI

Subject: Electronics Course Title: DSF-1005F2 Section – I Power Electronics

Subject. Electronies Cour.			land to Ministry	Sub-units planned
Month:	Month: January 2024		Module/Unit:	Definition, Need- semiconductor
Lectures	Practicals	Total	Unit 1:Power semiconductor	power devices, classification of
Lectures			devices GROUP C: (μC 8051 & PLC)	power semiconductor devices,
12	40	52	1. Study of Timers in 8051 μC.	Power diode: structure, operation,
				conductivity modulation, I-V
			2, 222,	characteristics, Reverse recovery
			interfacing to 8051 μC. 3. LCD Interfacing with 8051 μC	effect, series and parallel connection
				of diode, Power transistor: structure,
			4. 4.DC motor interfacing to 8051	operation, effect of drift layer.
			5. Stepper Motor interfacing to	specifications, Power MOSFET :
			8051	MOSFET structure, characteristics,
			6. DAC0808 interfacing to 8051	operation and drive circuits
			7. ADC0804 interfacing to 8051μC	
Month: F	ebruary 202	4	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit 2: Thyristors	Types of Thyristors, Structure of
12	40	52	GROUP C: (μC 8051 & PLC)	SCR, SCR Characterist
CA			8. Serial communication with PC	transistor analogy - Methods of
	on O Tagates	8323	using 8051 μC.	turning ON and turning OFF, dv/dt and di/dt protection, gate protection
	CO GRANA		9. Study of PLC Simulator	
REGMO	AOTUA		10. Programming with PLC for	
			sequential logic RS-FF,JK-FF	Diac and Triac: Basic structure,
			11. Programming with PLC for	working and V-I characteristic.
			sequential logic T-FF,D-FF	
			12. Study of PLC timers and	
			counters Module/Unit:	Sub-units planned
Month : N	1arch 2024		Wiodule/Offic.	Basics of single and three phase
Lectures	Practicals	Total	Unit 3: Controlled Rectifiers	supply phase and line voltage
12	40	52	Practicals: GROUP D: (AVR &	waveforms, SCR as a static switch,
			IoT)	phase controlled rectification, single
			13. Interfacing of Switches and LED	
			with Arduino.	phase half wave, full wave, bridge
			14. LCD Interfacing with Arduino	rectifiers with resistive & inductive
			15. Stepper Motor Interfacing with	loads.
			Arduino	
			16. Interface temperature sensor	
			LM35 with Arduino board	
			17. Interface temp. sensor &	
			Humidity Sensor (DHT11) with	
			Arduino	
			18. Accelerometer Sensor Interfacing	
			with Arduino.	
			19. Study the fundamental of IoT	
			Architecture, Arduino and	
Mosselle			necessary software	Sub-units planned
Month : A	prii 2024		Module/Unit:	Sub-units planned

Unit 4: Power Systems Practicals: GROUP D: (AVR and IoT) 11.Interface Bluetooth with Arduino 11.Interface Bluetooth with Arduino	Power Supplies: Switch mode power supply (DC): flyback, forward, half bridge and full bridge converters. Uninterrupted power supply (UPS), Electronic Ballast, Power factor correction.
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Mr. P.R. Bagade

Dr. C. B. Catil

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DEPARTMENT OF ELECTRONICS
VIVEKANAND COLLEGE, KOLHAPUR
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Department of Electronics Academic Year: 2023-24

Annual Teaching Plan

Name of the teacher: **Dr. P. S. Jadhav** Programme: B.Sc. I Semester- I

Subject: Electronics Course Title: DSC-II Course Code: DSC03ELE12 Digital Electronics-I

Month: July 2023			Module/Unit:	Sub-units planned
Lectures	Practical	Total	Lecture: Unit 1:Number System, Binary Codes and Binary Arithmetic	Decimal, Binary, Octal and Hexadecimal number systems and their inter
20	40	60	Practical's: 1. To familiarize with basic electronic components (R, C, L, diodes, transistors), Digital Multimeter, Function Generator, power supplies and Oscilloscope etc. 2. MeasurementofAmplitude,Frequency &PhasedifferenceusingOscilloscope 3. Verification of Kirchhoff's Laws	conversions.BCD code. ASCII code, Gray Code, Excess-3 Code, Binary Arithmetic: Addition, Subtraction by 1's complement and 2's complement method, Representation of signed and unsigned numbers
Month: Aug	gust 2023		Module/Unit:	Sub-units planned
Lectures Practical Total 20 20 20 20 20 20 20 20 20 20 20 20 20			Lectures: Unit 2:Logic Gates, Boolean algebra: Practicals: 1. Verification of Thevenin's Theorem. 2. Verification of Norton's Theorem 3. Verification of Superposition Theorem	Study of logic Gates: OR, AND, NOT, NOR, NAND, XOR, XNOR, Universal Gates, Boolean identities and Law's.Fundamental, theorems of Boolean algebra. Standard representation of logic functions (SOP and POS),
Month: Sept	tember 2023		Module/Unit:	Sub-units planned
Lectures	Practical	Total	Lectures: Unit 2:Logic Gates, Boolean algebra: Practicals:	Minimization Techniques (Karnaugh map minimization up to 4variables for
20	40	60	 Study of the I-V Characteristics of P-N unction Diodes Study of the a] breakdown Characteristics of Zener Diode b] Zener Diode as voltage regulator. Study of Half wave and Full wave rectifier(centre tapped transformer/bridge) 	SOP).Arithmetic Circuits: Binary Addition. Half and Full Adder. Half and Full Subtractor, 4-bit binary Adder/Subtractor.
Month: October 2023			Module/Unit:	Sub-units planned
Lectures 1	Practical	Total	Unit 3:Logic Families	Logic Families: Types of



20		40	60	 Study of IV Characteristics of JFET Study of Input, Output and transfer Characteristics of CE configuration of BJT Study of Voltage divider bias circuit for CE mode 	Logic Families, Characteristics of Logic Families, TTL NAND gate, TTL NOR gate, TTL NOT gate, Concept of Tristate Logic, MOS Technology, CMOS: NOR,NAND and NOT gates, Comparison of TTL and CMOS logic families
Month:	Month: November 2023			Module/Unit:	Sub-units planned
Lectu res	Practi cal	Total	50	Unit 4:Combinational circuits: Practicals: 1. Transistor as a switch	Multiplexers: - 2 to 1, 4 to 1 and 8 to 1.Demultiplexer: - 1 to 2,1 to 4, 1 to 8. Encoder: concept of encoder, Decimal to
				 Study of the RC Phase Shift Oscillator Study of the Wein Bridge Oscillator Study of the Colpitt's oscillator Study of the Hartley oscillator 	BCD Encoder. Basic Binary decoders: 2 to 4 line, 3 to 8 line and 4 to 16 line, BCD to decimal decoder, Study of BCD to seven-segment decoder driver IC 7447.

Dr. P. S. Jadhav

Dr. C. B. Patil

HEAD DEPARTMENT OF ELECTRONICS VIVEKANAND COLLEGE, KOLHAPUR (AUTONOMG JS)





Department of Electronics Academic Year: 2023-24

Annual Teaching Plan

Name of the teacher: **Dr. P. S. Jadhav** Programme: B.Sc. I Semester- II

Subject: Electronics Course Title: DSC-IV Course Code: DSC03ELE22 Digital Electronics-II

Mon	th: Novembe	er 2023	Module/Unit:	Sub-units planned
T	D .: 1		Unit 1: Sequential Circuit	Concept of Flip-flop, RS, D and JK
Lectures	Practicals	Total	Practical: Group B	Flip-Flops Concept of Clock, Level
10	20	30	1. Study of Logic Gates	and Edge Triggered RS, D, JK FF
			2. Study of Universal NAND Gate	
	. •		3. Study of Universal NOR Gate	
Mont	th: December	r 2023	Module/Unit:	Sub-units planned
Lectures	Practical	Total	Unit 1: Sequential Circuit	Preset and Clear operations. Race-
CHRONICE	MEMI OF ALL	NEW CONTRA		around conditions in JK Flip-Flop,
20	40	60	-	Master-slave JK Flip-Flop, T-Flip-
(2	II JMOMOTUM			flop.
			Unit 2: Shift registers and counters	Concept of register, Left shift and
			Practical:	Right Shift operations, Types of shift
			1. Study of De-Morgans Theorems	registers: SISO, SIPO, PISO & PIPO
			Study of Half Adder and Full	(only up to 4 bits).
			Adder	
			2. Study of Half and Full Subtractor	
			3. Study of BCD to seven segment	
			Decoder /s/	
16-	41. 7. 6	2024	10/	
Mon	th: January 2	2024	Module/Unit:	Sub-units planned
Lectures	Practical	Total	Unit 2: Shift registers and counters	Counters: classification of counters,
20	40	60	Practical:	Asynchronous counters: 3 bit ripple
			1. Study of Encoder	counter, Decade Counter.
			2. Study of Multiplexer (4:1) and	Synchronous Counter: 3 bit and
			Demultiplexer (1:4) using IC	decade synchronous counter. Ring
			3. Building and testing of RS Flip-	Counter and Johnson Counter
			Flop using NAND/NOR gate.	.Applications of Counters.
Mont	h: February 2	2024	Module/Unit:	Sub-units planned
Lectures	Practical	Total	Unit 3: Data Converters	4bit binary weighted and R-2R
20	40	60	Practical:	ladder network DAC: circuit and
			1. Building and testing D and JK	working.DAC Characteristics:
			Flip-Flop using IC	Accuracy and Resolution.
			2. Construction and study of Shift	
			Register (serial-in and serial-out)	
			3. Using D-type/JK Flip-Flop IC	
Mon	th: March 202	24	Module/Unit:	Sub-units planned
Lectures	Practical	Total	Unit 3: Data Converters	ADC: Flash, Countertype,
20	40	60	1. Study of 3-bit Asynchronous	successive approximation ADC,
20	10	50	counter Design and study of 4 bit	ADC Characteristics
			digital to analog converter using	
		,	R-2R ladder network.	COLLEGE
			2. Design and study of an Astable	25.19
			Multivibrator using IC 555Timer.	ESTD
				151 1111

Mor	nth: April 20	24	Module/Unit:	Sub-units planned	
Lectures 20	Practical 40	Total 60	 Unit 4:Study of Timer IC555 1. Design and study of a Monostable Multivibrator using IC555Timer. 2. Design and study of a Bistable Multivibrator using IC555Timer. 	IC555timer: Introduction, Block diagram, Astable, Monostable and Bistable multivibrator circuits	
Mor	nth: May 202	24	Module/Unit:	Sub-units planned	
Lectures	Practical	Total	Unit 4: Study of Timer IC555 1. Transistor as Switch 2. Kirchhoff's Laws	Applications of IC555: PWM, square wave generator and FSK	
8	10	18	2. Kirchhoff's Laws		

Dr. P. S. Jadhav

Dr. C. B. Patil

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Department of Electronics Academic Year: 2023-24

Annual Teaching Plan

Name of the teacher: **Dr. P. S. Jadhav** Programme: B.Sc. I Semester- II

Subject: Electronics Course Title: SEC-I: SEC03ELE21: Circuit simulation Lab

Month: November 2023			Module/Unit:	Sub-units planned
Lectures 0	Practicals 10	Total 10	Practicals 1. Determining the equivalent resistance of series resistors and parallel resistors 2. Study of Voltage Divider rule and Current Division rule 3. Verification /Validation of Kirchhoff's Current law and Kirchhoff's Voltage law 4. Verification/ Validation of Thevenin's theorem Module/Unit:	Sub-units
Monti	n : December	2023		planned
Lectures 0	Practicals 20	Total 20	 Verification/ Validation of Superposition theorem Verification/Validation of Norton's theorem Investigation of P-N Junction diode characteristics Study of Zener Voltage Regulator 	
Mon	th : January 2	024	Module/Unit:	Sub-units planned
Lectures 0	Practicals 20	Total 20	 9. Study of Half Wave Rectifier Circuit 10. Study of Full Wave rectifier Circuit with C filter/π filter 11. Study of Transistor as Switch 12. Study of Common Emitter Characteristics 	
Mont	h: February 2	024	Module/Unit:	Sub-units planned
ECL. YOU AR	eo nii 20 terati Doli gilalahin Diotua	20	 13. Design and assemble Single stage CE amplifier 14. Study of JFET Characteristics 15. Study of Hartley Oscillator/Colpitt's Oscillator 16. Study of the RC Phase Shift Oscillator 	
Mon	th: March 202		Module/Unit:	Sub-units planned
Lectures	Practicals	Total		

Department of Electronics Academic Year: 2023-24

Annual Teaching Plan

Name of the teacher: **Dr. Milind S. Patil** Programme: B.Sc. III Semester- V

Subject: Electronics
Course Title: Practicals

	Title: Practi	cals		
Month:	July 2023	1	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	
0	40	40	1. Study of Timers in 8051 Microcontroller. 2. LED, Switch and Relay interfacing to 8051 microcontroller. 3. LCD Interfacing with8051 Microcontroller. Project work	
Month: A	August 2023		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	
0	40	40	Group B: (Antenna and Power Electronics) 4. DC motor interfacing to 8051microcontroller.5. Stepper Motor interfacing to 8051microcontroller. 6. DAC0808 interfacing to 8051microcontroller. 7. ADC0804 interfacing to 8051microcontroller. Project work	
Month:	September 20	23	Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals:	
0	40	40	8. Serial communication with PC using 8051microcontroller GROUP D: (AVR and IoT) 1. Interfacing of Switches and LED with Arduino/AVR microcontroller. 2. LCD Interfacing with Arduino/AVR microcontroller. 3. Stepper Motor Interfacing with Arduino/AVR microcontroller.	
Month: C	October 2023		Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Practicals: 4. Interface temperature sensor LM35 with Arduino	
0	40	40	board and display temperature on LCD. 5. Interface temperature sensor and Humidity Sensor (DHT11) with Arduino/AVR board and display temperature and humidity values on LCD. 6. Accelerometer Sensor Interfacing with Arduino/AVR microcontroller. 7. Study the fundamental of IOT Architecture, Arduino and necessary software and create the thingspeak account. 8. Interface Bluetooth with Arduino and send the sensor data to smartphone through Bluetooth.	

Dr. Milind S. Patil



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Department of Electronics Academic Year: 2023-24



Annual Teaching Plan

Name of the teacher: Dr. Milind S. Patil Programme: B.Sc. III Semester- VI

Subject: Electronics Course Title: DSE1005 F1 Section II Advanced Microcontroller

3	Subject: Electronics Course Month: December 2023		Module/Unit:	Sub-units planned
Lectures	Practicals 40	Total 52	Embedded Systems Design: Introduction to AVR microcontroller:	1) What is embedded system, embedded system basic blocks, embedded
			Practicals: Group D: (AVR and IoT) 9. Interface Bluetooth with Arduino and receive the data from smartphone through Bluetooth to turn LED ON/OFF. 10. Interface wifi module with Arduino to upload sensor data to thingspeak cloud. 11. Interface wifi module with Arduino to retrieve data from thingspeak cloud. Project work	system hardware and software, embedded system characteristics, embedded system applications. 2) Overview of AVR family, ATmega8 pin configuration & function of each pin. AVR Microcontroller architecture, status register, Special function registers, SRAM, ROM & EEPROM space, On-Chip peripherals.
	anuary 2024		Module/Unit:	Sub-units planned
Lectures 12	Practicals 40	Total 52	3) AVR programming in C:	3) AVR Data types, AVR I/O port programming, Input capture and Wave Generator, PWM programming, External Interrupt programming, ADC programming, Serial Port programming.
			4) Peripheral interfacing and embedded system:	4) Interfacing of Switches, Relays, LEDs, seven segment display 16x2 LCD Interfacing, Stepper interfacing.
			Practicals: Group D: (AVR and IoT) 12. Interface GSM module with Arduino to upload sensor data to thingspeak cloud 13. Read the sensor data and upload the data to thingspeak cloud using NodeMCU	<u> </u>
GAEN OFFICE	DEPARTAEL		14. Study and implement MQTT protocol using Arduino Project work ESTD JUNE 1964	- 42

1	Month: February 2024			Module/Unit:	
1	Lectures	Practicals	Total		Sub-units planned
	Lectures	1140110415	Total	5) Designing of an Embedded System:	5) DC Motor speed control
		40	50		using PWM technique,
	12	40	52		Measurement of Temp. of
					an environment using
					sensor LM35, Dual channel
					Digital Voltmeter.
	Month: M	arch 2024		Module/Unit:	Sub-units planned
	Lectures	Practicals	Total	Practicals:	
				Group D: (AVR and IoT)	
	12	40	52	15. IoT Application Case study:	
				Home Automation	
				Project work	
				1 Toject work	

Dr. Milind S. Patil

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Department of Electronics Academic Year: 2023-24

Annual Teaching Plan

Name of the teacher: Dr. Milind S. Patil

Semester- VI Programme: B.Sc. III

Course Title: SEC 4: Embedded System Design using Arduino Subject: Electronics

Month : D	Month: December 2023		Module/Unit:	Sub-units planned Interfacing Arduino to LED's-
Lectures	Practicals	Total	3) Display	1.1: Ling single L.E.D. DIIIIKIII
6	0	6	Interfacing	multiple LED's, 7 segment display
Month: Ja	nuary 2024		Module/Unit:	Sub-units planned
6	O : CA 3H	6	3) Display Interfacing	traffic light, LED flashes, LED dot matrix, Interfacing to LCD's-Basic LCD control, display a message on LCD display
SURFICIAL TO S	BOST LIGHT CARRIE	ngill u	26 1 1 77 1	Sub-units planned
Month: Fo	ebruary 2024		Module/Unit:	Sensors- Definition, Types.
6	0	6	4) Interfacing sensors:	Interfacing Arduino to different sensors- light sensor, temperature sensor, humidity sensor,
Month: M	arch 2024		Module/Unit:	Sub-units planned
6	0	6	4) Interfacing sensors:	Pressure sensor, sound sensor, distance ranging sensor, water/detector sensor, smoke, gas, alcohol sensor, ultrasonic range finder.

Dr. Milind S. Patil

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Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage programme: B.Sc. I Semester- I

Subject: Electronics Course Title: ANALOG ELECTRONICS-I

Subject: Electronics Course Title: ANALOG ELECTRONICS-I						
Month: A	ugust 2023		Module/Unit:			
Lectures	Practicals	Total	Unit-1:Basic Circuit Elements Practicals:	Sub-units planned Study of basic circuit elements and passive components:		
12	20	32	Group A: (LIC & PLC) 1. Instrumentation amplifier using OPAMP 2. Precision rectifier using OPAMP 3. Log amplifier using OPAMP	Resistor, Capacitor, Inductor, Transformer, Relays, Switches (working principle, circuit symbols, types, specifications and applications		
Month: S	eptember 20	23	Module/Unit:	Sub-units planned		
Lectures	Practicals	Total	Unit-2:Circuit Analysis:	Concept of Voltage and		
12	20	32	Practicals: Group A: (LIC & PLC) 4. Study of active filter: Low and High pass 5. Study of active filter: band pass 6. Study of V to F and F to V using PLL.	Current Sources, Internal resistance, Kirchhoff's Current. Kirchhoff's Voltage Law, Mesh Analysis, Node Analysis, Principle of Duality, Superposition Theorem, Thevenin's Theorem, Norton's Theorem, Maximum Power Transfer Theorem.		
Month:	October 202	3	Module/Unit:	Sub-units planned		
Lectures	Practicals	Total	Unit-3:PN Junction Diode: Practicals:	Construction of PN junction, Formation of Depletion Layer,		
12	20	32	Group A: (LIC & PLC) 7. Study of PLC Simulator and implementing Boolean function 8. Programming with PLC for sequential logic RS -FF,JK-FF,T-FF,D-FF 9. Study of PLC timers and counters in PLC	Barrier potential, Forward and Reverse bias, Diode Equation and I-V characteristics, Zener diode, Zener and Avalanche breakdown, Zener diode specifications. Photo diode. Light Emitting Diode (LED): construction and working, 7-segment display and it's applications		
Month	: November	2023	Module/Unit:	Sub-units planned		
12	20	32	Unit-4:DC Power Supply ESTD JUNE 1964 1964	Need of PowerSupply, Block diagram of DC regulated power supply, Rectifiers: Halfwave, Fullwave rectifiers (center tapped and bridge):- Circuit diagrams, working and waveforms Zener diode as voltage regulator, Three pin IC regulators: Block diagram, Specifications and applications. Fixed and Variable voltage IC regulator		

Mr. M. B. Jirage

DEPARTMENT OF ELECTRONICS

Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage Programme: B.Sc. I Semester- II

Subject: Electronics Course Title: ANALOG ELECTRONICS-II

Subject: Electronics Course Title: ANALOG ELECTRONICS-II Month: January 2024 Module/Unit: Sub-units planned					
Month:	Month: January 2024		Module/Unit:	BJT: Introduction, Structure,	
Lectures	Practicals	Total	Unit-1: Bipolar Junction Transistor Practicals:	Working of transistor. Transistor configurations: CB, CE and CC	
12	20	32	Group A: (LIC & PLC) 1. Instrumentation amplifier using OPAMP 2. Precision rectifier using OPAMP 3. Log amplifier using OPAMP	configurations. CB, CB and CB configurations, characteristics of transistor in CE and CB configurations, Regions of operation (active, cut off and saturation), Current gains α and β. Relations between α and β, dc load line and Q point.	
Month: F	ebruary 202	4	Module/Unit:	Sub-units planned	
Lectures	Practicals	Total	Unit-2: Unipolar Devices	JFET: Construction, working and	
12	20	32	Practicals: Group A: (LIC & PLC) 4. active filter: Low and High pass 5. Study of active filter: band pass 6. V to F and F to V using PLL.	I-V characteristics (output and transfer), MOSFET: Construction, working and I-V characteristics (output and transfer).UJT: introduction, structure and characteristics.	
Month: N	March 2024		Module/Unit:	Sub-units planned	
Lectures	Practicals	Total	Unit-3 Amplifiers:	Need of transistor Biasing,	
12	20 h : April 202	32	Practicals: Group A: (LIC & PLC) 7. Study of PLC Simulator and implementing Boolean function 8. Programming with PLC for sequential logic RS -FF,JK-FF,T-FF,D-FF 9. Study of PLC timers and counters in PLC	Transistor biasing and Stabilization circuits- Fixed Bias and Voltage Divider Bias. Thermal runaway, stability and stability factor S., Class A, B, AB and C Amplifiers Single stage CE amplifier: Current gain, Voltage gain, Power gain, input and output resistances, frequency Response. Cascaded Amplifiers: Two stage RC, LC, TC and DC Coupled Amplifiers	
		24	Module/Unit:	Sub-units planned	
12	20	32	Unit-4: Feedback Amplifier and Oscillators: ESTD JUNE 1964	Concept of feedback, negative and positive feedback, advantages of negative feedback (Qualitative only). Oscillators: Barkhausen criterion for sustained oscillations. Phase shift, Wein Bridge, Hartley and Colpitt's oscillator. UJT as relaxation oscillator.	
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Mr. G. B. Jirage

Dr. C. B. Patil

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Department of Electronics

Name of the teacher: Mr. G. B. Jirage Programme: **B.Sc. I Semester-I**

Subject: Electronics Course Title: OEC-I: Course Code: OEC03 ELE11: CIRCUIT FUNDAMENTALS-I

Month: July 2023			Module/Unit:	Sub-units planned
Lectures	Practicals	Total	Unit1: Electricity	Introduction, Electric current, Electrical resistance, conductance and conductivity, open and short
12	0	12		circuit, Direct Current, Alternating current, Ohm's law, electrical energy, electrical Power, power dissipation in resistance
Month: A	ugust 2023		Module/Unit:	Sub-units planned
Lectures 12	Practicals 0	Total 12	Unit 2: D.C. circuit	Introduction, current direction and voltage polarity of circuit components, voltage notation, resistances in series, Voltage division rule, resistances in Parallel, Total power in series and parallel circuits, series parallel circuits.
Month: September 2023			Module/Unit:	Sub-units planned
Lectures 12			Unit 3: Kirchhoff's Laws and Theorems	Kirchhoff's laws- Kirchhoff's voltage and current law, Thevenin's theorem, Superposition theorem, Norton's theorem
Month: October 2023		Module/Unit:	Sub-units planned	
Lecture		Total	Unit 4: AC fundamentals	Introduction, types of alternating current or voltage, terms related to AC- cycle, time period,
12	0	12		frequency, Amplitude (Peak value),
Within . November 2025			Module/Unit:	Sub-units planned Peak to peak values, Instantaneous value, RMS
Lectur			Unit 4: AC fundamentals	value, Average value, concept of phase difference,
12	0	12		Harmonics

Mr. G. B. Jirage

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Department of Electronics Academic Year: 2023-24 Annual Teaching Plan

Name of the teacher: Mr. G. B. Jirage Programme: B.Sc. I Semester- II

Subject: Electronics Course Title: OEC-III OEC03ELE21 Circuit Fundamentals-II

2001			7. 1. 1. /T.L.:A.	Sub-units planned	
Month: December 2024		2024	Module/Unit:	Introduction resistors- specifications,	
Lectures	Practicals	Total	Unit 1: Passive circuit elements-I:	classification of resistors, construction of carbon composition resistors, wire	
12	00	12		wound resistors, film resistors,	
				variable resistors: potentiometers,	
				rheostat, thermistors, colour code	
				system for resistor, Capacitor: concept	
				of capacitance, types of capacitors.	
Mon	th: January 2	2024	Module/Unit:	Sub-units planned	
12	00	12	Unit 2: Passive circuit	Introduction, inductors, Concept of	
			elements-II	self-inductance, construction and	
				types of inductors. concept of mutual	
				inductance, construction and working	
	1 1			of transformer, types of transformers,	
Mont	h. Fab	2024	N. 1.1./TT.'.	Wires: different types of wires	
Month: February 2024			Module/Unit:	Sub-units planned	
12	0	12	Unit 3: Circuit controllers	Introduction, switch, switch action, types of switches, commercially available switches, fuses, fuse ratings, circuit breaker, relays: electromagnetic relay construction and working	
Month: March 2024			Module/Unit:	Sub-units planned	
12	0	12	Unit 4: Voltage and	Introduction, batteries, regulated DC	
			Current sources	supplies, solar cell, generators,	
1				oscillators and signal generators,	
			233 (63)	internal resistance of source, concept	
			CUEGE	of voltage source, ideal and practical	
			To correct of	voltage sources. concept of current source, ideal and practical current	
-			F CTD E	sources.	
			ESTD JUNE 1964	-3041003.	
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Mr. G. B. Jirage

Dr. C. B. Patil

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Department of Electronics Academic Year: 2023-24

Annual Teaching Plan

Name of the teacher: **Mr. G. B. Jirage** Programme: B.Sc. III Semester- V

Subject: Electronics Course Title: DSE 1005E2 Industrial Process Control

March	1 0000		T	
Month: July 2022				
Lectures 12	Practicals	Total 12	Module/Unit: Unit 1: Introduction of control system Unit2: Components of Control System	Sub-units planned 1) Significance Transfer Function, Types and order of transfer function (Open loop and Close loop transfer system), Block diagram of Control System and reduction rules, Basic elements of control system, open loop controlsystem, closed loop control system, control system terminology, manually controlled closed loop systems, automatic controlled closed loop systems, comparison closed-loop system and open-loop control, feed-forward control system, adaptive control system, classification of control system. ON-OFF controller, proportional control, PI controller, PD controller and PID control. Introduction to Fuzzy Controller 2) Op-amp as a zero crossing detector, non-inverting comparator, inverting comparator, two position control using op-amp, proportional controller, integral controller using Op-amp, derivative controller, PI controller, PID controller. Sub-units planned
Month: August 2022			Module/Unit: Unit 3	Programmable logic controller (PLC) basics:
Lectures 12	Practicals 0	Total 12	:Introduction to PLC Practicals:	Definition, overview of PLC systems, block diagram of PLC, input/output modules, power supplies, isolators, features like scan time, system scale, user interface. Modular PLC and Redundant PLC and Applications. Industrial Communication Buses: RS485, Profibus .Distributed control system, DCS components/block diagram, SCADA, adaptive control system.
Month: Sept 2023			Sub-units planned	
Lectures	Practicals	Total	Unit 4:Ladder	4) Basic components: fuse, pushbutton, selector switches, limit switches,
12	0	12	Programming basics	indicators, relay, timedelay relays functions and symbols. General PLC programming procedure programming on-off inputs/ outputs. ESTD Auxiliary JUNE 1964
	october 2023		Unit 4:Ladder	Auxiliary (\$1 JUNE (\$)

12	0	12	Programming basics	commands and functions: PLC Basic Functions: Register basics, timer functions, counter functions. Ladder Programming: Programs for Boolean logic and flip-flops, counters, timers, flasher. Application program Bottle filling plant, elevator control, washing machine control.
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Mr. G. B. Jirage



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